1. INTRODUCTION

The following sections will provide step-by-step instructions to explain how to get started with the Evaluation Kits for Audio DSP products from Cirrus Logic. This is a generic document as its principle applies to all current Audio DSP families from Cirrus Logic — the CS48500, the CS470xx Audio SoCs, and the CS49531x / 4970xx.

An advanced audio processing design is created and will be used as an example to illustrate the practical work with the DSP Composer™ tool and a CDB48560 evaluation board. The given example is more complex than the example projects included in the evaluation board software package. It also shows how to use the pre-defined blocks and primitives when using the DSP Composer GUI.

A Q&A section is included at the end of this document.

2. GETTING STARTED

2.1 Up-front information and preparation

Please prepare the following essential information about your target application before contacting Cirrus Logic:

- The basic technical design requirements including any 3rd party IP support such as the support of specific multichannel-audio formats or standards.
- The general project schedule and anticipated volume information.
- Your contact details.

With these details on hand, your local Cirrus Logic Distribution partner or sales offices can easily identify the 'best fit' Audio DSP for your application. Cirrus Logic offers a great variety of Audio DSP-products, which are widely compatible — whether your design needs a cost-effective, entry-level audio DSP such as a CS48500-DSP or a high-performance multichannel audio decoder such as the CS49700.

If applicable, Cirrus Logic needs to contact the relevant third party to get approval prior to providing samples or software.

Please contact your local Cirrus Logic sales representative or Distribution partner for details.

The evaluation board software is provided on the Cirrus Logic web site http://www.cirrus.com. Please refer to the web site section specific to the DSP of your choice and browse to the Resources section.

Within the software suite, you will only have access to modules and functions for which you have appropriate licences in place. If you require access to disabled blocks, you will need to obtain the licences from the third-party licensor first before Cirrus Logic is authorized to send you the specific code.

Cirrus Logic provides comprehensive technical documentation such as evaluation board manuals, data sheet specifications, or application notes which will be installed during the evaluation board software setup. The default path for all documents is C:\CirrusDSP\doc. Note that application notes for the individual processing blocks such as for third-party, IP-based decoders are provided with the individual setup files.

All Cirrus Logic DSPs are SRAM-based and can be upgraded in the field or during evaluation.
2.2 The Essential Set-up

The evaluation boards are connected to standard PCs with Windows XP® operating systems via USB. After a successful installation of the evaluation board software, the DSP Composer GUI can be started. Your computer screen should look like the following:

The GUI can also be used in emulation mode (without any evaluation board connected to the host PC).

Please refer to the CDB48500-USB Evaluation Kit guide from Cirrus Logic for a basic introduction to the minimum hardware set-up. This is available at http://www.cirrus.com. These specifications are also accessible per the START ⇒ Program Files ⇒ Cirrus Logic DSP menu after installation.

2.3 Using the DSP Composer GUI, Project Example

This section shows how to build a project with multiple audio I/Os and some customized audio processing using the DSP Composer GUI.

Let us consider designing an audio system with the following features:

- 4 Analog Inputs, 6 Analog Outputs
- Customized Audio Processing Chain as Follows:
  - High-pass and low-pass filters for the input channels
  - Mixer with 4 inputs to create 6 output channels
  - 6-channel, 4-band parametric EQ
  - Sine wave test-tone generator
First, drag the \textit{SYSTEM} block from the menu on the left-hand side. When positioning the \textit{SYSTEM} block on the virtual workspace or canvas, some parameters can be set. Please leave everything set to default for now.

Next, we define the audio inputs and outputs by another simple drag-and-drop operation. For this example, let us select analog inputs for the input block in the \textit{Properties} menu and some analog outputs per the default settings.
Your workspace or canvas should now look similar to this:

Our example project does not include any matrix decoders or virtualizing modules.

Therefore, we select the corresponding *Passthru-Matrix* processing module (MPM) and *Passthru-Virtual* processing module (or VPM) from the selection menu on the left-hand side to allow a simple pass-through function.

The next step is to start with creating our own custom Post Processing module (PPM). Drag the Custom PPM block on to the canvas. Here we have named the block *My PPM*. We also need to add another final stage — the audio manager module, which can be found under *Standard PPM elements*. This connects the *My PPM* module to the audio outputs.
The top-level diagram now looks like this:

Now let us add another page to the Post Processing block to keep the top-level PPM diagram clean. To do this, we need to open the custom PPM block named My PPM by double-clicking it. Then move your mouse pointer to the bottom of the canvas and right-click to select Insert to add another page. After double-clicking on it, you may also want to rename the page to something such as Processing.
Our workspace or canvas now looks like this:

Let us return to the Schematic page now. We will be using so-called fly-offs to connect elements between the Schematic and the Processing page.

The creation of new fly-offs for the design's input and output signals is easy: Mark the first four input channels L, C, R, and Ls on the Schematic page using your mouse. Then, change to the Wire mode using <CTRL>++<W>, left-click, hold and drag four wires and then click the right mouse button. This will result in four blue labels. Double-click these to give them some individual names. Apply the same procedure to the six output channels: L, C, R, Ls, Rs, and Lb.
Our schematic now looks like this:

Now, mark and copy all fly-offs marked in blue and insert them into the *Processing* page. Refer to Section 5.1 in the DSP Composer manual for more details on how to use fly-offs and associated wiring techniques.

**NOTE:**

DSP Composer verifies that fly-offs used in a design are properly connected in the fly-offs-page accessible in the lower left corner, see the screenshot above.

You will also notice that unused signals in standard elements such as the PPM block will be marked green, whereas connected channels will appear black during the wiring process.

The next step is to add some audio filters and other audio processing functions for our design example. Double-click on the Custom PPM Elements menu on the left to expand and browse through the various element categories such as filters, mixers, and other functions which are referred to as Primitives in the documentation.

Drag-and-drop the following pre-defined blocks onto the workspace:

- A high-shelf filter and a low-pass 18 dB / Octave filter from the Filters section
- A multi-band parametric EQ available in the filters' *m-band, n-band PEQ* menu. Enter 6 channels and 4 bands per channel into the pop-up box.
- A Sine Wave Generator from the Generators selection.
- A signal-presence detector taken from the Meters menu
- A 4 x 6 channel mixer from the Mixers' Custom menu. Enter the corresponding number of channels into the pop-up box.
- A 5 inputs, 4 outputs router from the Routers' Custom menu. Enter the number of I/Os into the pop-up box.
Here is what the workspace looks like now:

Next, we need to create additional three instances of the high-shelf and low-pass filter blocks. Simply select the individual blocks, press <CTRL>+<D> (for duplication), and enter the required number of blocks to be reproduced. Re-arrange all blocks and use <CTRL>+<W> to change to Wire mode, then connect all elements. Refer to the DSP Composer Manual Data Sheets DS704 for more advanced wiring modes and interconnection details.

As a result, we have created the following audio processing chain. The 5 x 4 router allows an easy assignment of the sine wave generator signal to test individual channels. The presence detectors are for monitoring purposes of either test tones or input signals which are digitized by the on-board CODEC.
The canvas or workspace should now look like this:

You will notice that there are no limitations how to interconnect or arrange the individual processing stages or primitives. As long as the audio processing fits the DSP’s internal resources, the design engineer has the ultimate freedom to create any customized audio processing stage to meet the design requirements.

Now that the customized audio stage, My PPM, is finished, we need to complete the top-level schematic diagram.

Once the wiring is completed, the top-level schematic will look like this. You may also want to add a text label or bump panel taken from the Graphics Elements and Blocks section at the very bottom of the selection menus on the left-hand side of the GUI.
Once our project is complete, save it. To load our project onto the appropriate evaluation board, we need to build and compile it.

To do this, verify that the evaluation board is connected to your PC via USB and powered up, then click CONNECT and GO. The DSP Composer GUI will enter the gesture mode and your mouse pointer will turn into a hand icon, which allows the adjustment of control settings.

The DSP Composer GUI will download the audio processing configuration to the CDB48500 evaluation board allowing you to evaluate the functions.

Consult the CDB48500-USB Evaluation Kit Guide for more details on the board setup and the DSP Composer section for the basic processing blocks or elements, wiring details, and the so-called gesture mode.
3. Q & A

Q: Where can I find more technical documents for your Audio DSP Evaluation boards and tools?

A: Evaluation board manuals including schematics can be downloaded from our web site [http://www.cirrus.com](http://www.cirrus.com). Application notes for individual processing blocks and further documentation are available either from your local Cirrus Logic sales office or after the installation of the Evaluation Board Software.

Q: I am having difficulties connecting your Audio DSP evaluation board to my PC and/or I am seeing communications error messages.

A: We recommend carefully checking any firewall software or similar programs. If you see any message about allowing `coyote_proxy_server` to run, be sure to allow it.

This blue button, labeled CDM for Cirrus Device Manager, should be visible in your PC’s taskbar and should indicate your evaluation board as an active device.

If the button is grey, but not blue, you need to verify the installation and the evaluation board detection. Refer to the Troubleshooting Guide in the DSP Evaluation Kit Guide.

Secondly, check the following: Browse to the `C:\CirrusDSP\bin` folder and search for `*.bit` files. There should be only one `usbfpga_xyz.bit` file. Delete any other file, if it exists.

Q: What if I need a DSP function that is not included in the supplied modules?

A: There are three methods to integrate customized audio processing blocks into DSP Composer:

1) Define your own audio processing block using DSP Composer as explained in Step 3) Using the DSP Composer GUI, Project Example above. Low-level, pre-defined elements or primitives are available, such as single FIR filters which can be used to implement your individual design.

2) Contact Cirrus Logic to create a new audio processing block for you.

3) Use the Cirrus Logic Integrated Development Environment (CLIDE) tool set to compile your own algorithms using C or assembler. See below.

Q: We need to program the Audio DSP using C or assembler to incorporate proprietary processing. How will Cirrus Logic support this?

A: Cirrus Logic provides a tool chain named CLIDE (Cirrus Logic Integrated Development Environment) to support both programming in C as well as at the assembler level.
REVISION HISTORY

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>REV2</td>
<td>MAR 2011</td>
<td>Corrected minor formatting issues.</td>
</tr>
</tbody>
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Contacting Cirrus Logic Support
For all product questions and inquiries contact a Cirrus Logic Sales Representative.
To find one nearest you go to http://www.cirrus.com

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