
CS82L44 Equivalent IO Circuits

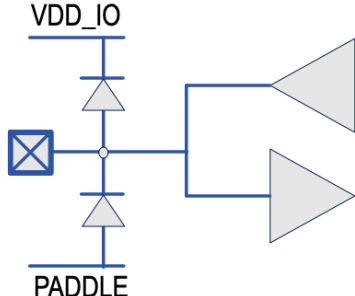
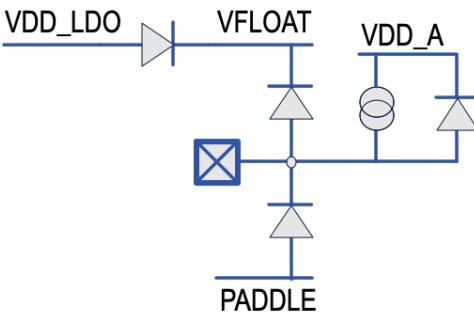
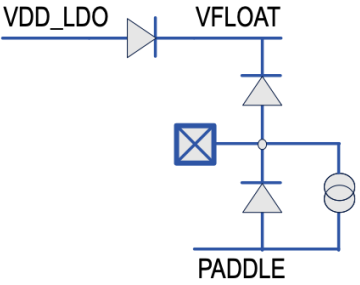
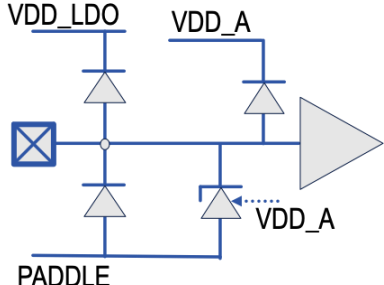
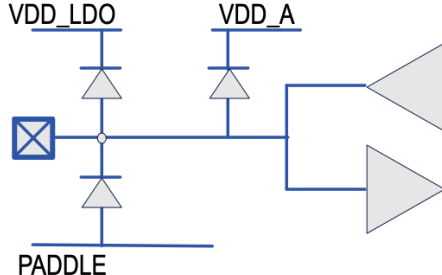
Introduction

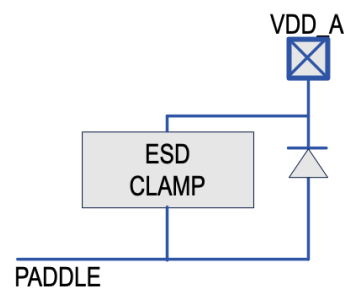
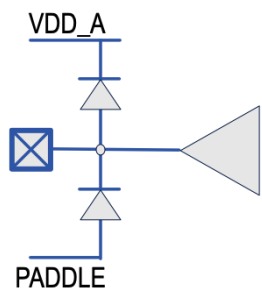
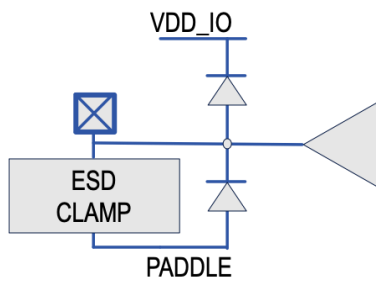
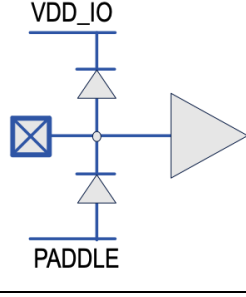
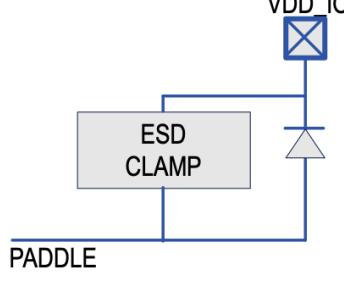
To provide customers with a greater insight into the input and output structures of the CS82L44 Four-Channel AFE with Sensor Timing Generation and LVDS/CMOS Data Output, a table detailing the equivalent on-chip circuit for each input and output of the device is presented.

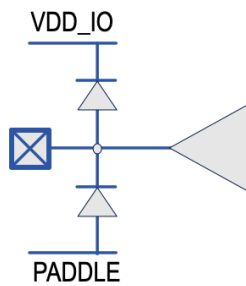
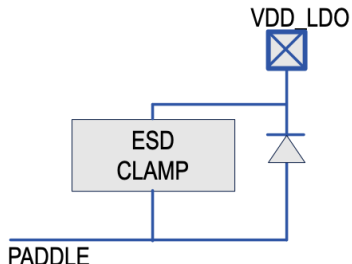
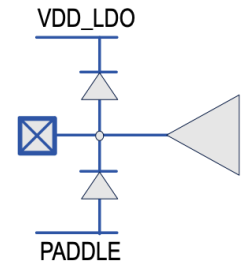
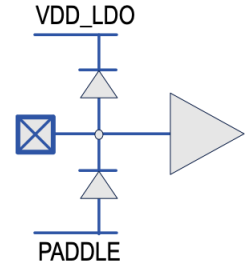
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1 Equivalent IO Circuits for CS82L44

Pin Name	Pin #	Power Supply	I/O	Equivalent Circuit
CLKOUT2/LEDG_EN/LED_START CLKOUT3/LEDB_EN/RSMP_EXT SPI_SDI/I2C_SDA SPI_SDO/I2C_SCL TGSYNC1/VSMP_EXT CLKOUT4 CLKOUT1/LEDR_EN/TGSYNC2	40 1 36 35 37 38 39	VDD_IO	I/O	
ILED_CTRL	2	VDD_LDO, VDD_A	O	
ILED_R ILED_G ILED_B	3 4 5	VDD_LDO	I	
IN1 IN2 IN3 IN4	6 7 8 9	VDD_LDO, VDD_A	I	
VBIAS	10	VDD_LDO, VDD_A	I/O	

Pin Name	Pin #	Power Supply	I/O	Equivalent Circuit
VDD_A	15	VDD_A	-	
VREF_FILT VLDO1_FILT VLDO3_FILT	16 17 18	VDD_A	O	
VLDO2_FILT	19	VDD_IO	O	
SPI_SCK SPI_CS DSLCT MCLK_EXT_N MCLK_EXT/MCLK_EXT_P	34 33 20 31 32	VDD_IO	I	
VDD_IO VDD_IO	21 30	VDD_IO	-	

Pin Name	Pin #	Power Supply	I/O	Equivalent Circuit
DCLKOUT_N/DCLKOUT2 DCLKOUT_P/DCLKOUT1 DOUT3_N/DOUT6 DOUT3_P/DOUT5 DOUT2_N/DOUT4 DOUT2_P/DOUT3 DOUT1_N/DOUT2 DOUT1_P/DOUT1	22 23 24 25 26 27 28 29	VDD_IO	O	
VDD_LDO	11	VDD_LDO	-	
VLDO5	13	VDD_LDO	O	
LDO_EN	14	VDD_LDO	I	

2 Revision History

Revision	Changes
R2 JUN 2025	<ul style="list-style-type: none"> Updated pin 14 pin name to correspond to device revision B0
R1 JUL 2024	<ul style="list-style-type: none"> Initial version.

Contacting Cirrus Logic Support

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