
CS82L46 Equivalent IO Circuits

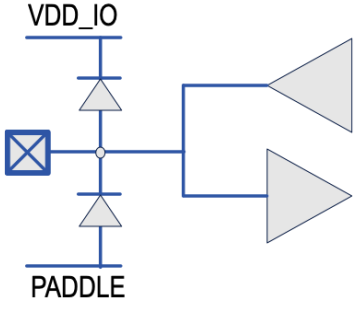
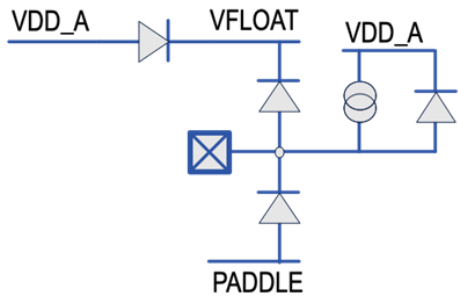
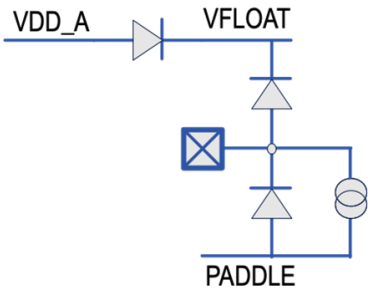
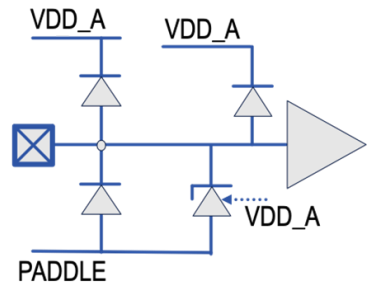
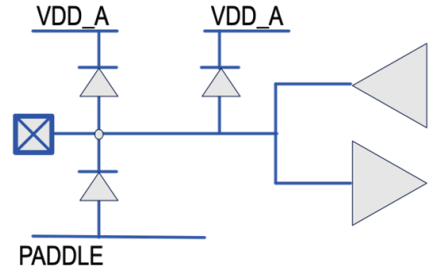
Introduction

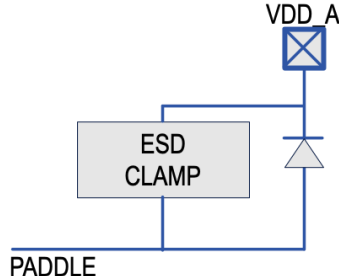
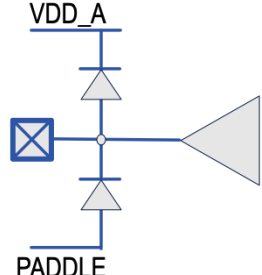
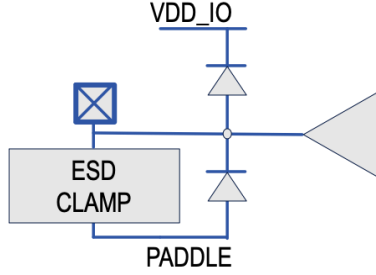
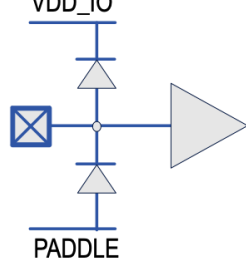
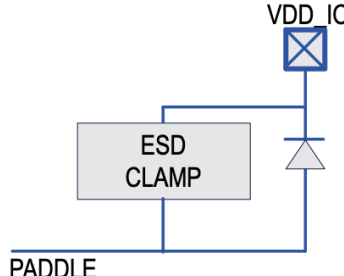
To provide customers with a greater insight into the input and output structures of the CS82L46 Six-Channel AFE with Sensor Timing Generation and LVDS/CMOS Data Output, a table detailing the equivalent on-chip circuit for each input and output of the device is presented.

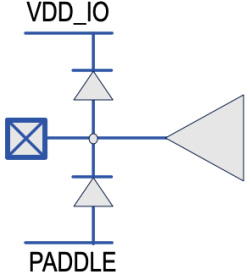
Table of Contents

1	Equivalent IO Circuits for CS82L46.....	2
2	Revision History.....	4

1 Equivalent IO Circuits for CS82L46

Pin Name	Pin #	Power Supply	I/O	Equivalent Circuit
CLKOUT2/LEDG_EN/LED_START CLKOUT3/LEDB_EN/RSMP_EXT SPI_SDI/I2C_SDA SPI_SDO/I2C_SCL TGSYNC1/VSMP_EXT CLKOUT10 CLKOUT9 CLKOUT8 CLKOUT7 CLKOUT6 CLKOUT5 CLKOUT4 CLKOUT1/LEDR_EN/TGSYNC2	1 2 19 20 40 41 42 43 44 45 46 47 48	VDD_IO	I/O	
ILED_CTRL	3	VDD_A	O	
ILED_R ILED_G ILED_B	4 5 6	VDD_A	I	
IN1 IN2 IN3 IN4 IN5 IN6	7 8 9 10 11 12	VDD_A	I	
VBIAS	13	VDD_A	I/O	

Pin Name	Pin #	Power Supply	I/O	Equivalent Circuit
VDD_A	14	VDD_A	-	
VREF_FILT VLDO1_FILT VLDO3_FILT	15 16 17	VDD_A	O	
VLDO2_FILT	18	VDD_IO	O	
SPI_SCK SPI_CS DSLCT MCLK_EXT_N MCLK_EXT/MCLK_EXT_P	21 22 23 38 39	VDD_IO	I	
VDD_IO VDD_IO	24 37	VDD_IO	-	

Pin Name	Pin #	Power Supply	I/O	Equivalent Circuit
DOUT5_N/DOUT10	25	VDD_IO	O	
DOUT5_P/DOUT9	26			
DOUT4_N/DOUT8	27			
DOUT4_P/DOUT7	28			
DCLKOUT_N/DCLKOUT2	29			
DCLKOUT_P/DCLKOUT1	30			
DOUT3_N/DOUT6	31			
DOUT3_P/DOUT5	32			
DOUT2_N/DOUT4	33			
DOUT2_P/DOUT3	34			
DOUT1_N/DOUT2	35			
DOUT1_P/DOUT1	36			

2 Revision History

Revision History

Revision	Changes
1.0 JUL 2024	<ul style="list-style-type: none"> Initial version.

Contacting Cirrus Logic Support

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