The analog THD+N performance, at 0 dBFS input, varies from channel to channel and decreases at higher temperatures and higher VA voltages. At +5 V VA supply and 25°C THD+N is typically -87 dB on the worst channel.

Adding the recommended 130 kΩ resistor (See Figure 1.) from the FILT+ pin to GND improves the typical THD+N to -90 dB on the worst channel. The minimum guaranteed THD+N across all channels, temperature and voltage using the recommended resistor is -78 dB.

Figures 2 through 4 show the channel variance, voltage and temperature drift when using the recommended resistor on FILT+.

---

The MUTEC mute control pin goes active after only 512 LRCK periods of static zero data.

Operation at +3.3 V is not offered for this revision. The Specified Operating Conditions are as follows:

<table>
<thead>
<tr>
<th>Parameters</th>
<th>Symbol</th>
<th>Min</th>
<th>Nom</th>
<th>Max</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>DC Power Supply</td>
<td>VA</td>
<td>4.75</td>
<td>5.0</td>
<td>5.25</td>
<td>V</td>
</tr>
<tr>
<td>Specified Temperature Range</td>
<td>TA</td>
<td>-10</td>
<td>-</td>
<td>+70</td>
<td>°C</td>
</tr>
</tbody>
</table>

The Popguard® ramp-down function will occur if either MCLK is removed or RST is asserted. See Figure 5 for details.
Figure 2. Typical Channel-to-channel Variance with Recommended Resistor on FILT+

Figure 3. Typical Variance Over Temperature with Recommended Resistor on FILT+

Figure 4. Typical Variance Over Voltage with Recommended Resistor on FILT+
Figure 5. CS4361 Rev A0 Initialization and Power-Down Sequence