3) populate R39 with 0-ohms
2) remove R28
1) replace C38 with 0 Ohm resistor

For external MCLK:
1) replace C8 with 0 Ohm resistor
2) remove R28
3) populate R39 with 0-ohms

For external MCLK:
1) replace C8 with 0 Ohm resistor
2) remove R28
3) populate R39 with 0-ohms for external VCP_FILT +/- supplies

DAC I2C Address
I2C Write/Read: 0x62/0x63

External Amplifier Power

XLR Differential Output

3.5mm Headphone Input Jack

On-Board Load

Interrupt LED
**Aardvark header**

**I2C level shifter**

**Device ID EEPROM with Unique S/N**

**Io Expander**

- **I2C Address**
  - I2C Write/Read: 0x44/0x45

**RST and INT HEADER**

- **NOTE:** External RST and INT will be unbuffered and needs to be at DUT I/O level

**I2C Header**

- **NOTE:** External I2C will be unbuffered and needs to be at DUT I/O level
For the 28-Pin package, the 3.3V output from the external regulator has to be connected to VREG, VCC_A and VCC_D. The VCC pin has to be left open with no connection. From the external input 3.3V, 1.8V is internally generated for the chip’s internal usage.

GANG must be pulled-up/down with 100K because pin becomes an output shortly after RESET.
**Programmable Delay Reset**

- Output valid at VDD of 0.8V (worst case)
- Release delay = 20 ms
- SENSE threshold (neg, typ) = 0.925V x 0.9125 = 0.9175V
- SENSE threshold (neg, max) = 0.925V x 0.9375 = 0.9519V
- V_T (neg) = 0.7VDD = 3.5V
- Internal pull-up resistance = 5kOhms typ

- \( \text{RESET release delay} = 20 \text{ ms} \)
- \( \text{SENSE threshold (neg)} = \left( \frac{10.2K}{8.06K} + 1 \right) \times 0.405V = 0.9175V \)
- \( \text{SENSE threshold (pos, typ)} = 0.9175V \times 1.0175 = 0.9336V \)
- \( \text{SENSE threshold (pos, max)} = 0.9175V \times 1.0375 = 0.9519V \)
- \( \text{RESET output valid at VDD of 0.8V (worst case)} \)

- \( \text{MR V_IL (max)} = 0.3 \times \text{VDD} = 1.5V \)
- \( \text{MR V_IH (min)} = 0.7 \times \text{VDD} = 3.5V \)
- \( \text{MR internal pull-up resistance} = 90k\Omega \text{ typ} \)

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**CIRCUIT DIAGRAM**

[Diagram of Programmable Delay Reset with component labels and connections]