WM8804_6152_DS20_EV1_REV2
Schematic and Layout
SPDIF Output

S/PDIF Output and Matching Network

Load Impedance = 75R

J21

Block

C48

0.1uF

R57

110Ω

R54

210Ω

TX0_OUT

S/PDIF Output
NOTE 1: DAC output signal level @ 0dBFS input = -3dBV.
NOTE 2: Outputs are line-level drive only (not to be used to drive headphone or speaker loads).
USB Power / Level Shift

USB Interface to WM8804 Reset Level Shift

Remove H2 1->2 In 2-Wire Mode

USB Interface MCU to WM8804 Control Interface Level Shift
Power
Mechanical
APPLICATION SUPPORT

If you require more information or require technical support, please contact the Wolfson Microelectronics Applications group through the following channels:

Email: apps@wolfsonmicro.com
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Mail: Applications Engineering at the address on the last page

or contact your local Wolfson representative.

Additional information may be made available on our web site at:

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