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## **Technical Bulletin: CS5531/32/33/34 rev H1 Silicon**

(Reference CS5531/32/33/34-AS data sheet revision DS289F4 dated APR '07).

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### **Description:**

This document describes Performance Improvements made in the CS5531/32/33/34-AS rev H1 devices.

Revision H1 of the CS553x-AS family of devices feature a number of improvements of which users should be aware to take full advantage of the enhanced performance now available in these devices.

- 1) Issues related to operation of the on-chip oscillator in revisions C, D, and E (documented in Errata ER289E1) have been addressed.

Revision H1 devices do not demonstrate this behavior.

- 2) The Input Offset Drift specification has been changed to reflect improved performance in the Modulator. At lower gain settings, users will experience a significant improvement in performance due to this enhancement.

This specification remains unchanged in "-BS" devices.

- 3) Product Specifications for both "-AS" and "-BS" devices are now provided as distinct documents to distinguish the capabilities of both product grades.

The industry-leading, low-noise performance of  $6 \text{ nV} / \sqrt{\text{Hz}}$  at 0.1 Hz available in the CS5532/34-BS devices is due to the super-low-noise, chopper-stabilized Programmable Gain Instrumentation Amplifier (PGIA). These devices provide up to 23 bits of noise-free resolution at a gain setting of 64.

Users will please note that there are no quality issues related to this revision and that a re-qualification was not required to upgrade to this revision under JEDEC standards.

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