### Evaluation Board for CS3318

#### Features
- Single-ended Analog Inputs
- Single-ended Analog Outputs
- Supports AC and DC-Coupled Analog I/O
- Flexible Serial Control I/O Headers
  - Serial Control Input Header to Accommodate On-board and External Serial Controllers
  - Serial Control Output Headers for “Chaining” Serial Control Across Multiple CDB3318 Evaluation Boards
  - 3.3 V Logic Interface Level
- Demonstrates Recommended Layout and Grounding Arrangements
- Minimal Power Supply Requirements
  - ±8 V to ±9 V and Ground
- Windows® Compatible Software Interface
  - Easy and Intuitive Graphical Interface to Channel and Master Volume Controls
  - Supports USB and RS-232 PC Connectivity

#### Description
The CDB3318 evaluation board is an excellent means for evaluating the CS3318 analog volume control. Evaluation requires an analog signal source and analyzer, power supplies, and a Windows PC compatible computer.

Standard RCA phono jacks are provided to easily interface external analog signals with the evaluation board. Each of the CS3318’s inputs and outputs may be independently AC or DC-coupled to their respective I/O connectors.

The Windows software provides a GUI to make configuration of the CDB3318 easy. The software communicates through the PC’s USB or Serial port to configure the control port registers so that all features of the CS3318 can be evaluated.

As an alternative to the serial control provided via a PC, the evaluation board may be configured to accept external control signals for operation in a user application during system development.

To facilitate multi-CS3318 system design efforts, headers are provided for connecting serial control signals between multiple CDB3318 evaluation boards.

#### Ordering Information
CDB3318 Evaluation Board

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![Diagram of CDB3318 Evaluation Board Connections](image-url)
TABLE OF CONTENTS

1. SYSTEM OVERVIEW ............................................................................................................. 3
   1.1 Power ............................................................................................................................ 3
   1.2 Grounding and Power Supply Decoupling ................................................................. 3
   1.3 CS3318 Analog Volume Control ................................................................................. 3
   1.4 External Control Headers .......................................................................................... 3
   1.5 Analog Inputs ............................................................................................................. 4
   1.6 Analog Outputs .......................................................................................................... 4
   1.7 PC Interfaces ............................................................................................................. 4

2. PC SOFTWARE CONTROL ...................................................................................................................... 5
   2.1 CS3318 Controls Tab ................................................................................................... 5
   2.2 Register Maps Tab ...................................................................................................... 6

3. PERFORMANCE PLOTS ....................................................................................................................... 7

4. SYSTEM CONNECTIONS & JUMPERS ............................................................................................... 8

5. CDB BLOCK DIAGRAM ............................................................................................................ .............. 9

6. CDB SCHEMATICS ............................................................................................................. ................. 10

7. CDB LAYOUT ................................................................................................................. ...................... 13

8. REVISION HISTORY ........................................................................................................... ................. 16

LIST OF FIGURES

Figure 1.CS3318 Controls Tab .................................................................................................. ................. 5
Figure 2.Register Maps Tab .................................................................................................... .................... 6
Figure 3.Frequency Response .................................................................................................................... 7
Figure 4.THD+N vs. Amplitude .................................................................................................. ................. 7
Figure 5.THD+N vs. Frequency .................................................................................................. ................ 7
Figure 6.Crosstalk ............................................................................................................ ........................... 7
Figure 7.Block Diagram ........................................................................................................ ....................... 9
Figure 8.CS3318 ................................................................................................................. 10
Figure 9.Analog Inputs and Outputs ............................................................................................ ............. 11
Figure 10.Serial Control / Power ..................... .......................................................................................... 12
Figure 11.Silkscreen Top ...................................................................................................... .................... 13
Figure 12.Top Side Copper Layer ............................................................................................ ............ 14
Figure 13.Bottom Side Copper Layer ............................................................................................ ............ 15

LIST OF TABLES

Table 1. System Connections ................................................................................................... .................. 8
Table 2. On-Board Switches ....................................................................................................... ................... 8
Table 3. System Headers ........................................................................................................ 8
1. SYSTEM OVERVIEW

The CDB4265 evaluation board is an excellent means for evaluating the CS3318 digitally controlled analog volume control. Analog audio signal interfaces are provided, an on-board microcontroller and USB/RS-232 PC interface is used for easily configuring the CS3318’s internal registers, and a USB cable is included for use with the FlexGUI Windows configuration software.

The CDB3318 schematic set is shown in Figure 8 through Figure 10.

1.1 Power

Power (±8 V to ±9 V) must be supplied to the evaluation board through the red VA+ and green VA- binding posts. An on-board regulator provides a 3.3 V supply to the digital circuitry. All voltage inputs must be referenced to the single black binding post ground connector (see the System Connections table on page 8).

WARNING: Please refer to the CS3318 data sheet for allowable voltage levels.

1.2 Grounding and Power Supply Decoupling

The CS3318 requires careful attention to power supply and grounding arrangements to optimize performance. Figure 7 on page 9 provides an overview of the connections to the CS3318. Figure 11 on page 13 shows the component placement. Figure 12 on page 14 shows the top layout. Figure 13 on page 15 shows the bottom layout. The decoupling capacitors are located as close to the CS3318 as possible. Extensive use of ground plane fill in the evaluation board yields large reductions in radiated noise.

1.3 CS3318 Analog Volume Control

A complete description of the CS3318 is included in the CS3318 product data sheet.

The required configuration settings of the CS3318 are achieved via its control port registers, accessible through the CS3318 tab of the Cirrus Logic FlexGUI software. A register-level configuration interface is provided on the Register Maps tab. See the "PC Software Control" section on page 5 for more information.

1.4 External Control Headers

The evaluation board has been designed to allow interfacing with external systems via the headers J17, J88, and J89.

The 15-pin, 3 column header, J17, provides bidirectional access to the CS3318’s SPI/PC® and MUTE digital control signals. The pins located in the column labeled “PC” connect to the on-board PC interface circuitry, and those in the column labeled “In” connect directly to the CS3318’s digital control I/O pins. By default, shunts are populated across these rows, connecting the PC interface to the CS3318’s digital control I/O. To use an external digital control source, simply remove the shunts and connect a ribbon cable to the “In” position. A single “GND” column for the ribbon cable’s ground connection is provided to maintain signal integrity. Two unpopulated pull-up resistors are also available should the CDB3318 be required to provide the pull-up function for the PC bus.

The 10-pin, 2 column header, J89, provides bidirectional access to the CS3318’s PC SDA and SCL signals, as well as unidirectional output access to the CS3318’s ADO, ENOut, and MUTE signals. The SDA and SCL signals are connected directly to their corresponding pins on the CS3318, as well as those on J17 described above. The ADO, ENOut, and MUTE signals are re-driven versions of these signals as present directly on their respective I/O pins on the CS3318. This header may be used to connect the serial control signals between 2 or more CDB3318’s (out of J89 on one and in to J17 on another) for multiple CS3318 PC serial control evaluation.
1.5 Analog Inputs

RCA connectors supply the CS3318 analog inputs through single-ended passive circuits with no input filtering. Refer to the CS3318 data sheet for the maximum input signal level.

Each analog input may be AC or DC coupled to its respective RCA connector. This selection is made via the 2-pin headers labeled "DC Couple Input" and placed adjacent to each input connector on the board. To DC couple an input connector, place a shunt across the respective header. To AC couple an input connector, remove the shunt from the respective header. By default, the input connectors are AC coupled to the CS3318's inputs.

1.6 Analog Outputs

The CS3318 analog outputs are routed through single-ended passive circuits with no output filtering and connected to RCA jacks for easy evaluation.

Each analog output may be AC or DC coupled to its respective RCA connector. This selection is made via the 2-pin headers labeled "DC Couple Output" and placed adjacent to each output connector on the board. To DC couple an output connector, place a shunt across the respective header. To AC couple an output connector, remove the shunt from the respective header. By default, the output connectors are AC coupled to the CS3318's outputs.

1.7 PC Interfaces

USB and RS-232 connections are provided to facilitate software control of the CS3318's internal registers. A graphical user interface is available for the CDB3318 to allow easy manipulation of the CS3318's internal registers. See the CS3318 datasheet for complete internal register descriptions.

To enable the CDB3318, simply connect the supplied USB cable from an available USB port on a PC to the USB connector (J37), or alternatively connect a 9-pin serial cable from an available COM port on a PC to the RS-232 connector (J42) and launch the Cirrus Logic FlexGUI software.

Refer to "PC Software Control" on page 5 for a description of the Graphical User Interface (GUI).
2. PC SOFTWARE CONTROL

The CDB3318 is designed for use with the Microsoft Windows based FlexGUI graphical user interface. This interface provides comprehensive control over the CS3318's internal registers via a PC's USB or RS-232 port.

The FlexGUI software may be downloaded and installed from www.cirrus.com/msasoftware.

Step-by-step instructions for using the FlexGUI are provided as follows:

1. Download and install the FlexGUI software from www.cirrus.com/msasoftware.
2. Connect the CDB3318 to a host PC using the supplied USB cable or a 9-pin serial cable.
3. Apply positive power, negative power, and ground to the VA+, VA-, and GND binding posts respectively.
4. Launch the FlexGUI software. The GUI will load and be displayed.
5. Un-check the “Power Down CS3318” check box to allow audio to pass through the device.

2.1 CS3318 Controls Tab

The CS3318 Controls tab provides a high-level intuitive interface to many of the configuration options of the CS3318. Control over the state of the CS3318's MUTE and RESET input pins is also provided.

Figure 1. CS3318 Controls Tab

The Master X Mask check boxes are used to map each channel to the master controls. These check-boxes reflect the state of their associated Master X Mask bits; when checked, the channel is un-masked.

The sliders facilitate control over the Ch X Volume registers, providing gain and attenuation in ½ dB steps.

The +¼ dB Gain check boxes are used to add an additional ¼ dB of gain to the volume level displayed for each channel.

See the CS3318 datasheet for complete internal register descriptions.
2.2 Register Maps Tab

The Register Maps tab provides an easy register-level interface to the on-board devices. Register values can be modified on a bit-wise or byte-wise basis. To modify a single bit, first select the register by clicking its position in the register matrix, then click the appropriate push-button for the desired bit. To modify an entire register, simply enter the register’s new value directly into the register matrix.

Within the Register Maps tab, the CS3318 tab is used to access the CS3318’s internal registers, and the GPIO tab is used to access the on-board microcontroller outputs used to control the CS3308’s MUTE input and default I²C address.

The Comm Mode Select box may be used to change the CS3318’s serial communication protocol between I²C and SPI. The CS3318 will be automatically reset when the communication protocol is changed.

The software is only capable of addressing the CS3318 at its default address. If the CS3318’s individual address is changed, the software will lose communication with the device until it is reset, thereby returning to its default address. See the CS3318 datasheet for more information about the device’s individual address.
3. PERFORMANCE PLOTS

\( T_A = +25 \, ^\circ C, \, V_A+ = +9 \, V, \, V_A- = -9 \, V, \, V_D = +3.3 \, V, \, R_L = 100 \, k\Omega, \, C_L = 20 \, \mu F, \, V_{IN} = 2 \, V_{RMS}, \, F_{IN} = 1 \, kHz, \) 

Gain = 0 dB, Measurement Bandwidth = 20 Hz to 20 kHz, unless otherwise noted.

![Figure 3. Frequency Response](image)

![Figure 4. THD+N vs. Amplitude](image)

![Figure 5. THD+N vs. Frequency](image)

![Figure 6. Crosstalk](image)
## System Connections & Jumpers

<table>
<thead>
<tr>
<th>Connector Name</th>
<th>Reference Designator</th>
<th>Signal Direction</th>
<th>Connector Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>In 1</td>
<td>J29</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 2</td>
<td>J27</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 3</td>
<td>J11</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 4</td>
<td>J15</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 5</td>
<td>J55</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 6</td>
<td>J57</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 7</td>
<td>J71</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>In 8</td>
<td>J73</td>
<td>Input</td>
<td>Analog input to CS3318.</td>
</tr>
<tr>
<td>Out 1</td>
<td>J19</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 2</td>
<td>J21</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 3</td>
<td>J46</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 4</td>
<td>J48</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 5</td>
<td>J62</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 6</td>
<td>J64</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 7</td>
<td>J79</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>Out 8</td>
<td>J81</td>
<td>Output</td>
<td>Analog output from CS3318.</td>
</tr>
<tr>
<td>USB IO In</td>
<td>J37</td>
<td>Input/Output</td>
<td>USB connection to PC for software control.</td>
</tr>
<tr>
<td>USB IO Out</td>
<td>J37</td>
<td>Input/Output</td>
<td>USB connection to PC for software control.</td>
</tr>
<tr>
<td>RS-232 In</td>
<td>J40</td>
<td>Input/Output</td>
<td>Serial RS-232 connection to PC for software control.</td>
</tr>
<tr>
<td>RS-232 Out</td>
<td>J40</td>
<td>Input/Output</td>
<td>Serial RS-232 connection to PC for software control.</td>
</tr>
<tr>
<td>I²C/SPI In</td>
<td>J17</td>
<td>Input/Output</td>
<td>Connection for internal/external I²C/SPI control signals.</td>
</tr>
<tr>
<td>I²C/SPI Out</td>
<td>J89</td>
<td>Output</td>
<td>Connection for internal/external I²C/SPI control signals.</td>
</tr>
<tr>
<td>I²C/SPI Out</td>
<td>J88</td>
<td>Output</td>
<td>Connection for internal/external I²C/SPI control signals.</td>
</tr>
<tr>
<td>SPI Out</td>
<td>J88</td>
<td>Output</td>
<td>Connection for internal/external I²C/SPI control signals.</td>
</tr>
<tr>
<td>C2</td>
<td>J36</td>
<td>Input/Output</td>
<td>Connection for programming the on-board microcontroller (U14).</td>
</tr>
</tbody>
</table>

### Table 1. System Connections

<table>
<thead>
<tr>
<th>Switch Name</th>
<th>Reference Designator</th>
<th>Switch Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>uC Reset</td>
<td>J5</td>
<td>Resets the on-board microcontroller (U46).</td>
</tr>
</tbody>
</table>

### Table 2. On-Board Switches

<table>
<thead>
<tr>
<th>Connector Name</th>
<th>Reference Designator</th>
<th>Header Function</th>
</tr>
</thead>
<tbody>
<tr>
<td>DC Couple Input 1</td>
<td>J3</td>
<td>When a shunt is present across its pins, each header DC couples its respective input connector to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 2</td>
<td>J5</td>
<td>When a shunt is present across its pins, each header DC couples its respective input connector to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 3</td>
<td>J16</td>
<td>When a shunt is present across its pins, each header DC couples its respective input connector to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 4</td>
<td>J18</td>
<td>When no shunt is present across the header’s pins, its respective input connector will be AC coupled to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 5</td>
<td>J58</td>
<td>When no shunt is present across the header’s pins, its respective input connector will be AC coupled to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 6</td>
<td>J59</td>
<td>When no shunt is present across the header’s pins, its respective input connector will be AC coupled to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 7</td>
<td>J74</td>
<td>When no shunt is present across the header’s pins, its respective input connector will be AC coupled to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Input 8</td>
<td>J75</td>
<td>When no shunt is present across the header’s pins, its respective input connector will be AC coupled to the associated input of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 1</td>
<td>J32</td>
<td>When a shunt is present across its pins, each header DC couples its respective output connector to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 2</td>
<td>J33</td>
<td>When a shunt is present across its pins, each header DC couples its respective output connector to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 3</td>
<td>J51</td>
<td>When a shunt is present across its pins, each header DC couples its respective output connector to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 4</td>
<td>J52</td>
<td>When no shunt is present across the header’s pins, its respective output connector will be AC coupled to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 5</td>
<td>J67</td>
<td>When no shunt is present across the header’s pins, its respective output connector will be AC coupled to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 6</td>
<td>J68</td>
<td>When no shunt is present across the header’s pins, its respective output connector will be AC coupled to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 7</td>
<td>J84</td>
<td>When no shunt is present across the header’s pins, its respective output connector will be AC coupled to the associated output of the CS3318.</td>
</tr>
<tr>
<td>DC Couple Output 8</td>
<td>J85</td>
<td>When no shunt is present across the header’s pins, its respective output connector will be AC coupled to the associated output of the CS3318.</td>
</tr>
</tbody>
</table>

### Table 3. System Headers
5. CDB BLOCK DIAGRAM

Figure 7. Block Diagram

- 8-Channel Passive Outputs
- AC/DC Coupling
- 8-Channel Digitally Controlled Analog Volume Control
- AC/DC Coupling
- 8-Channel Passive Inputs

USB/RS232 PC Interface
- PC or External Serial Control Input
- External Serial Control Output
7. CDB LAYOUT

Figure 11: Silkscreen Top
Figure 13. Bottom Side Copper Layer
8. REVISION HISTORY

<table>
<thead>
<tr>
<th>Release</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>DB1</td>
<td>Initial Release</td>
</tr>
<tr>
<td>DB2</td>
<td>Updated performance plots shown in Figures 3 - 6 on page 7.</td>
</tr>
</tbody>
</table>

Contacting Cirrus Logic Support
For all product questions and inquiries, contact a Cirrus Logic Sales Representative.
To find the one nearest to you, go to www.cirrus.com.

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