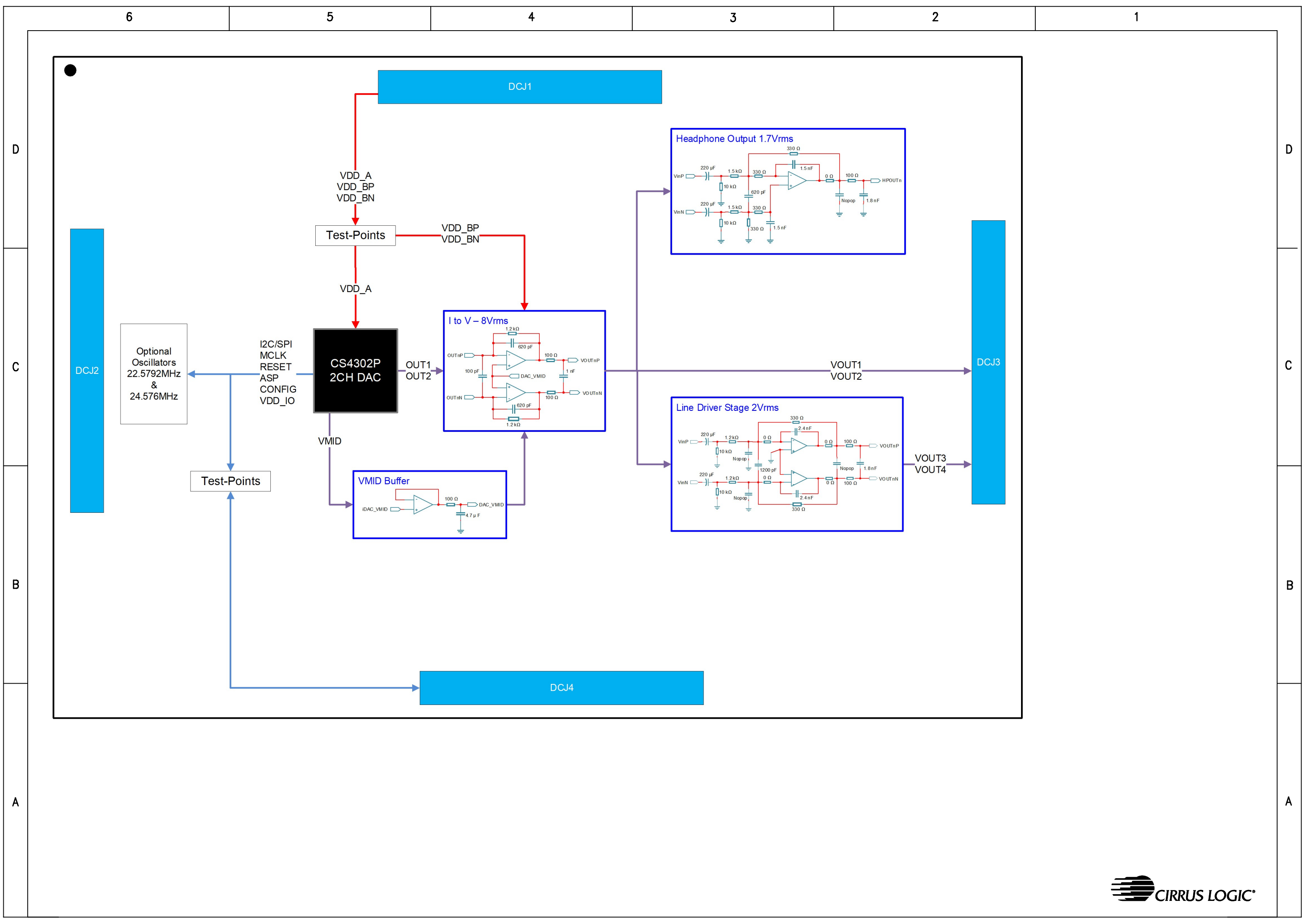
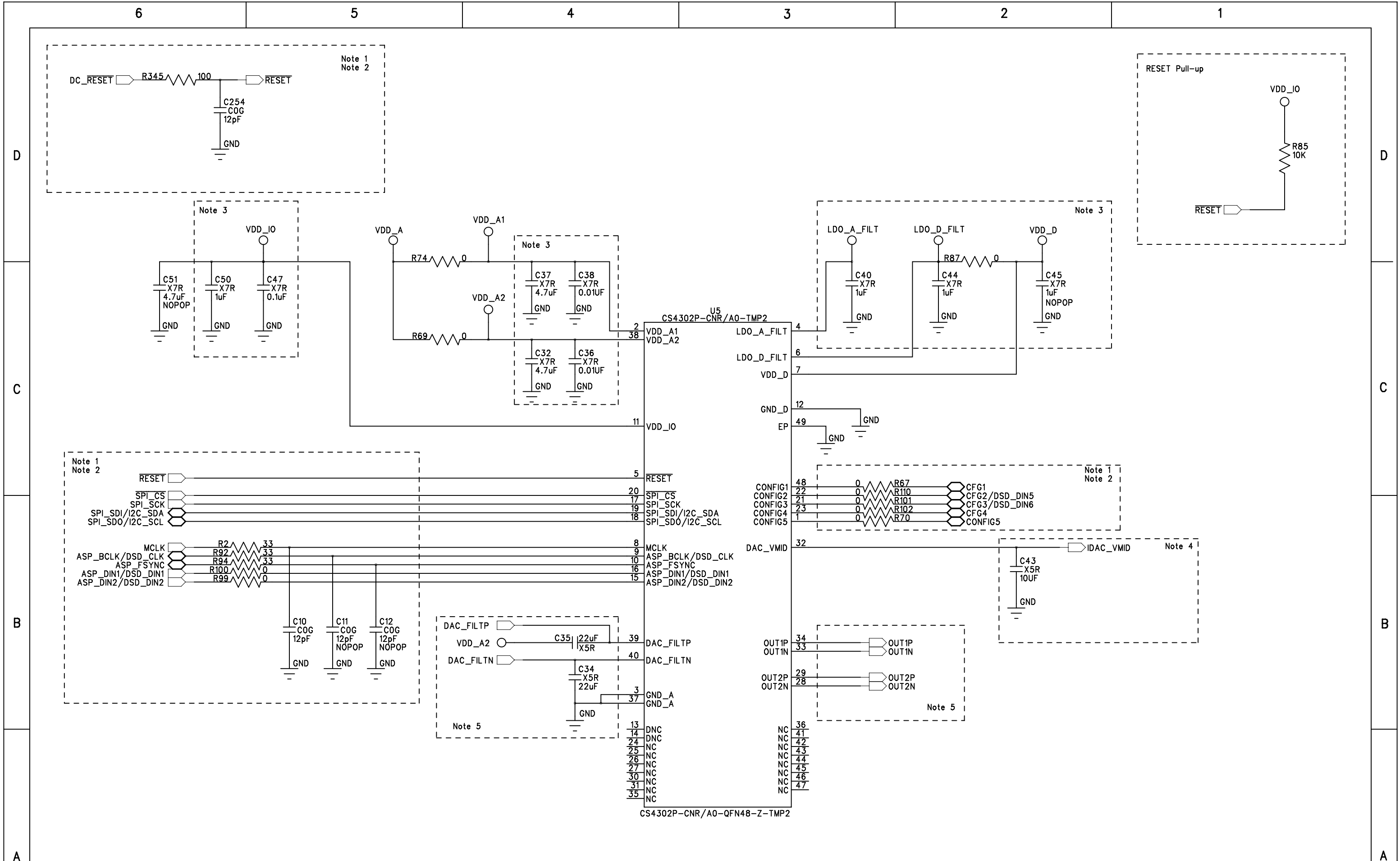


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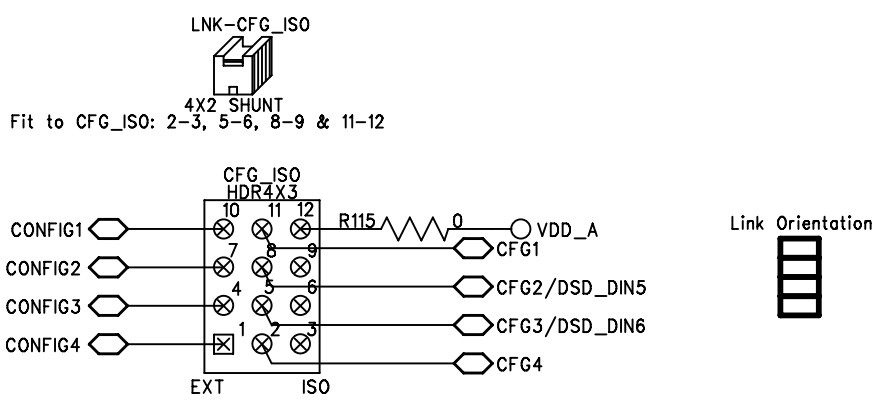
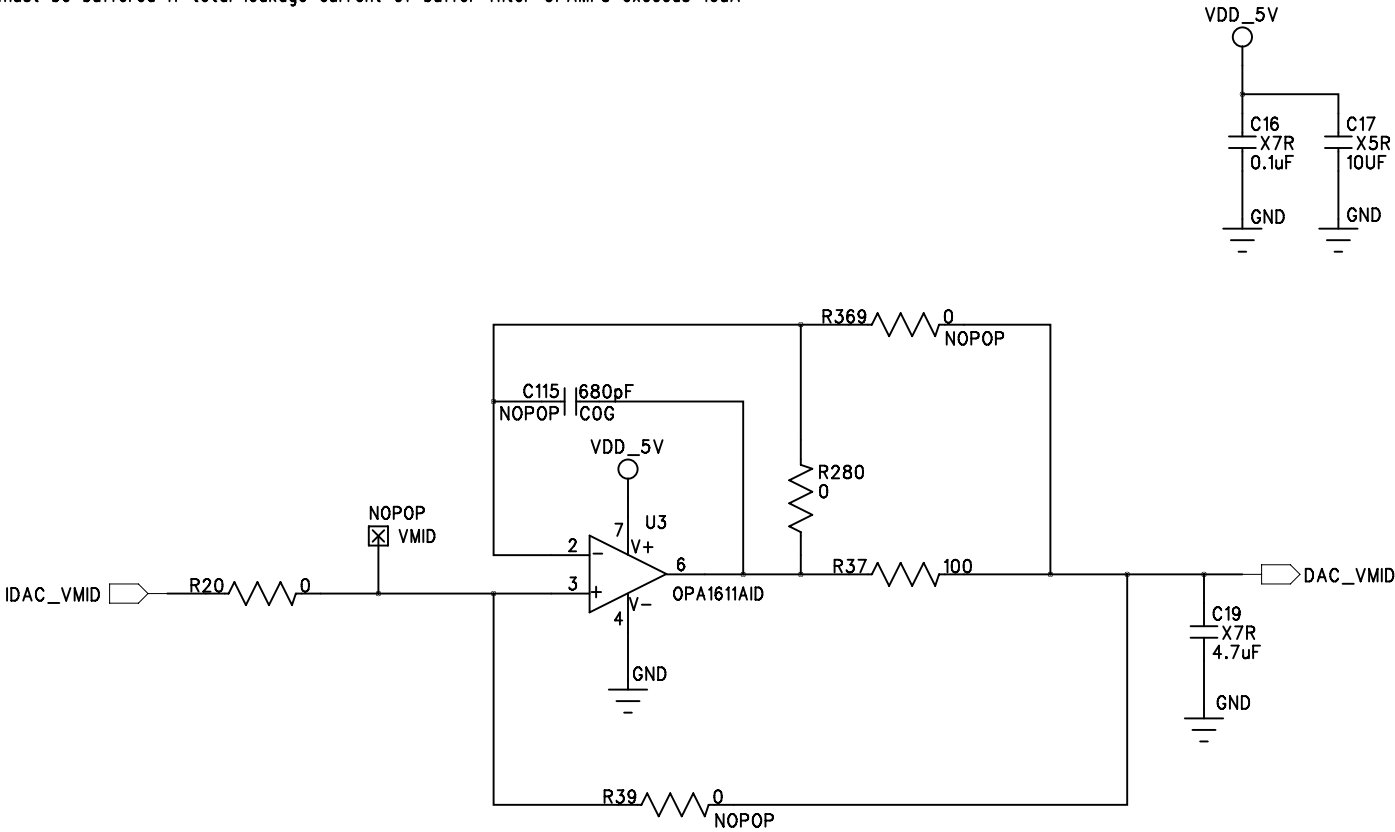
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VMID Buffer
Vmid must be buffered if total leakage current of buffer filter OPAMPs exceeds 10uA

Note 3



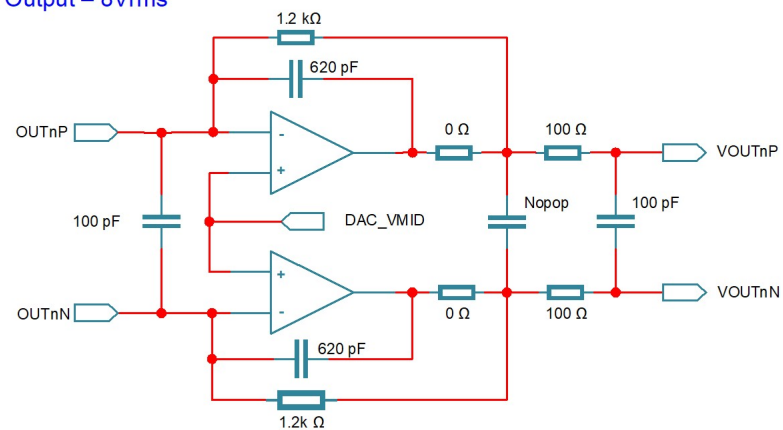
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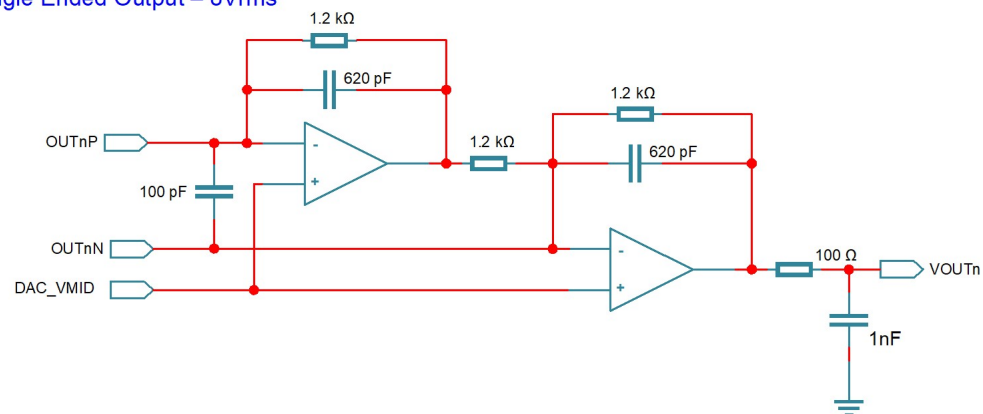
Output Buffer/Filter Options

I to V Op-Amp Circuits

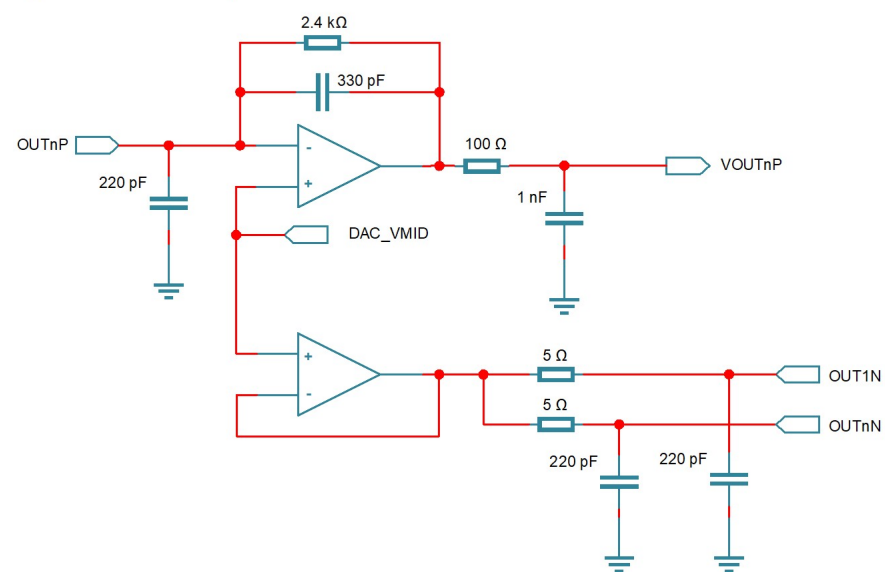
Differential Output – 8Vrms



Single Ended Output – 8Vrms

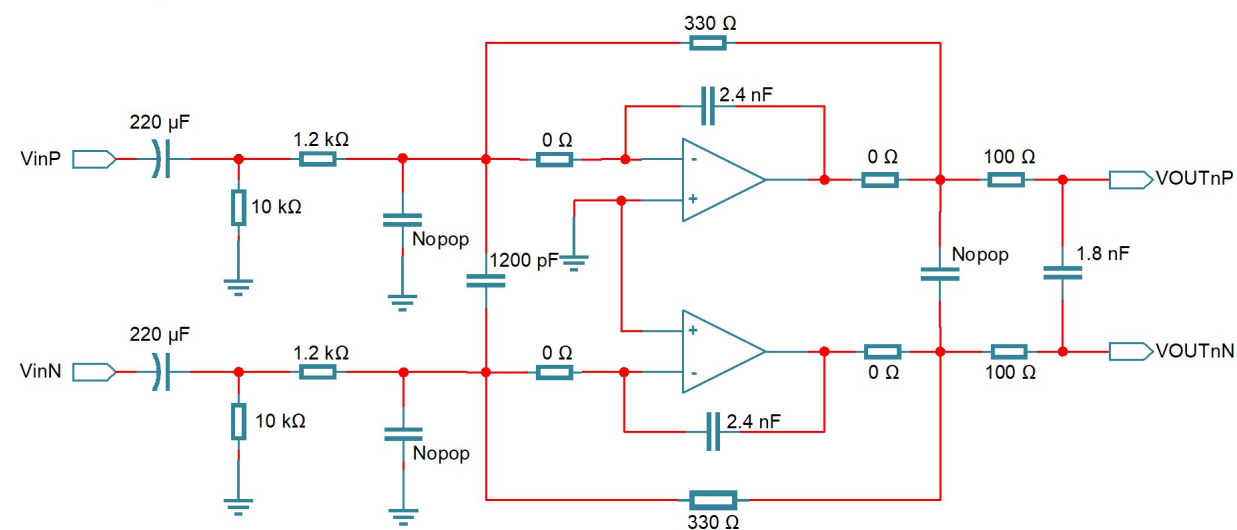


Single Ended Half Output – 8Vrms

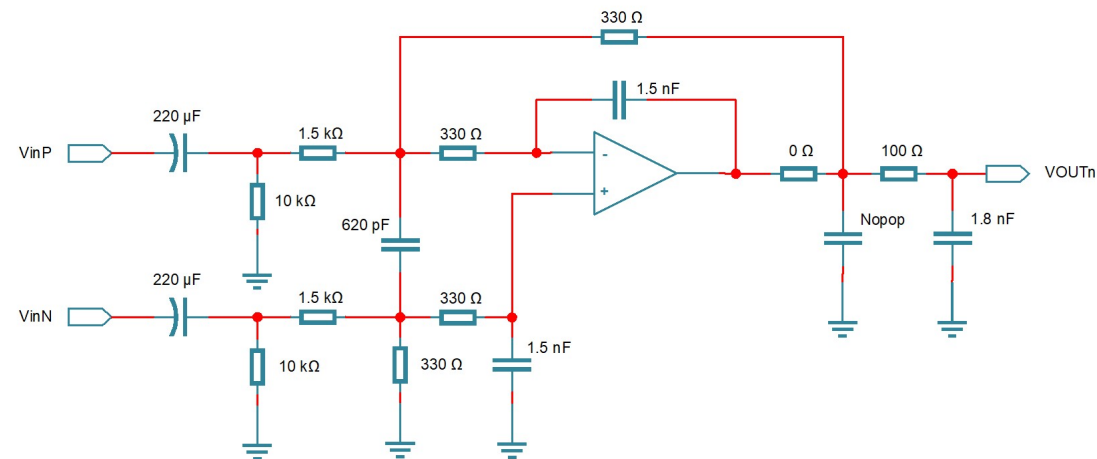


Additional Op-Amp Circuits

2Vrms Line Output



Headphone Driver Output – 1.7Vrms



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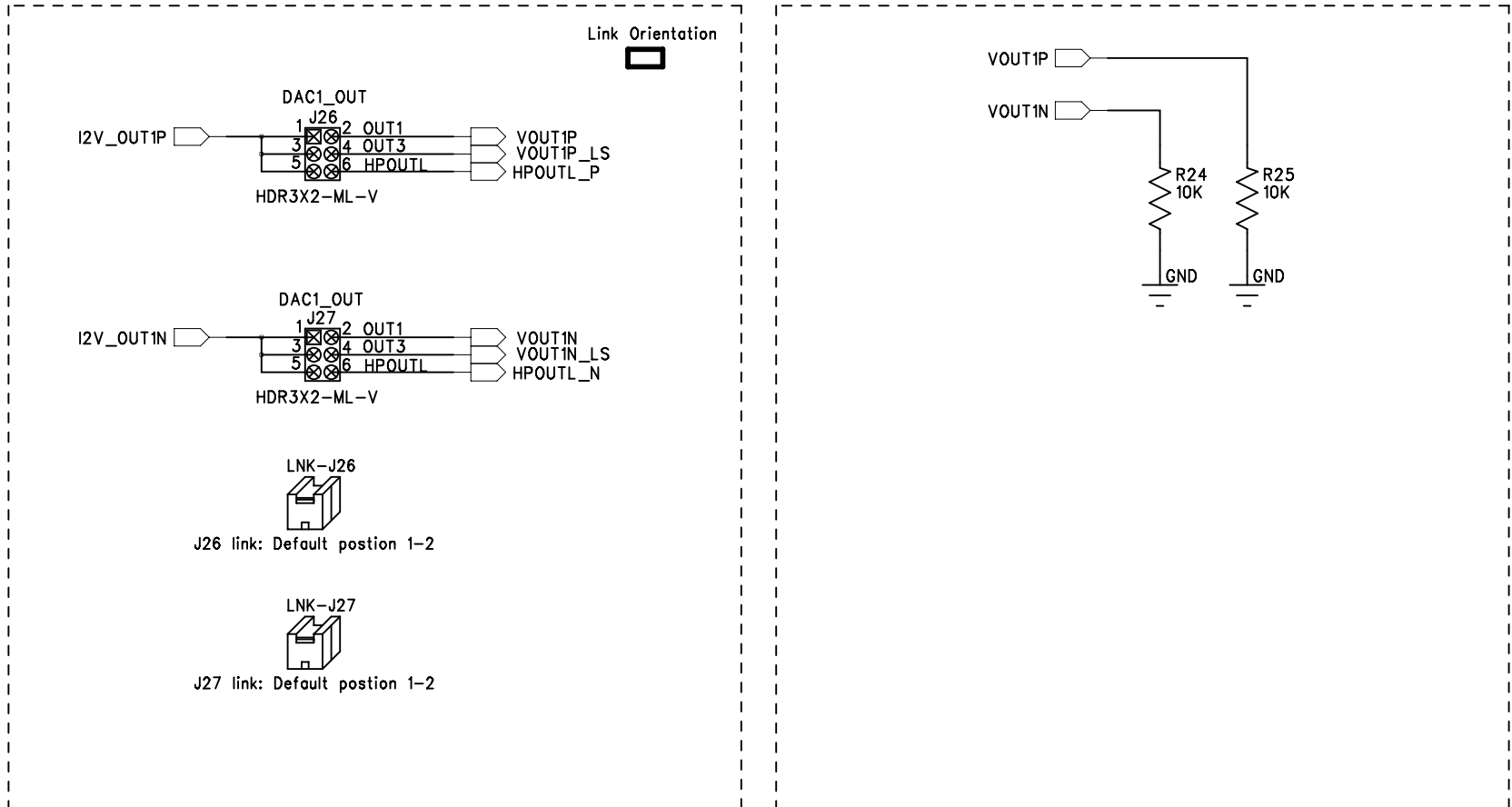
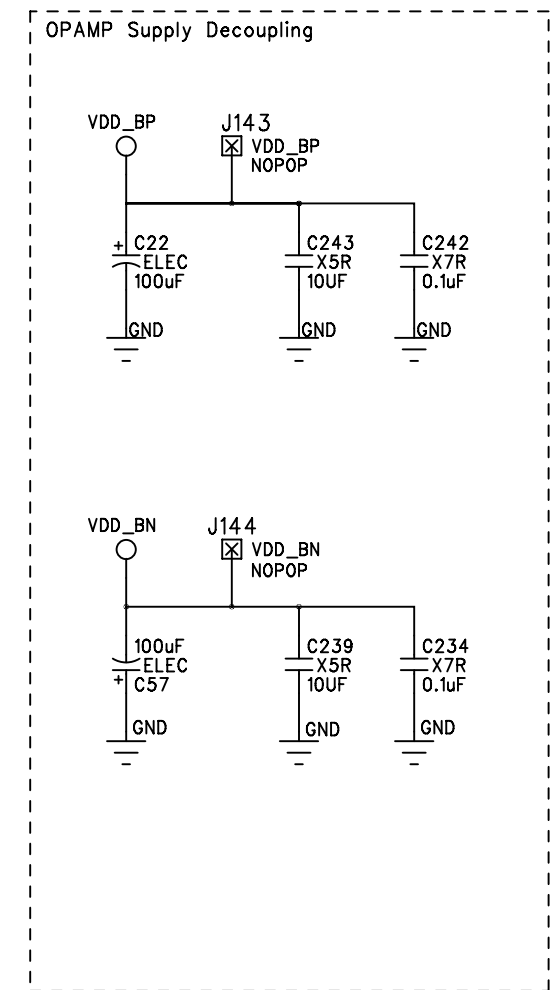
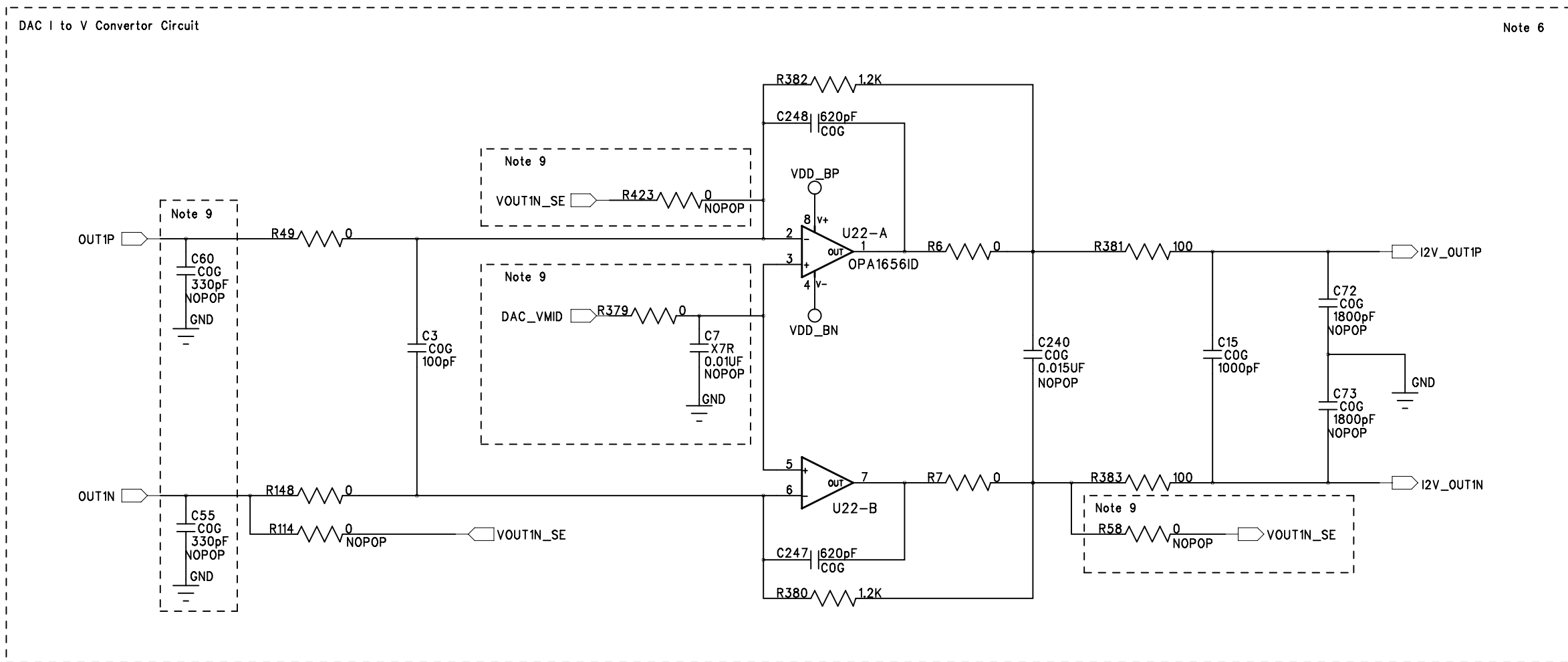
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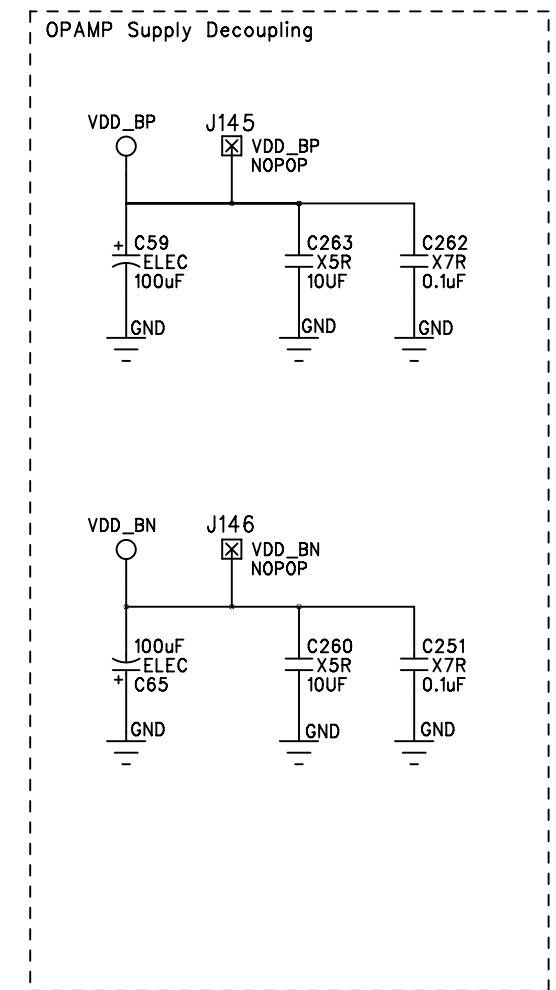
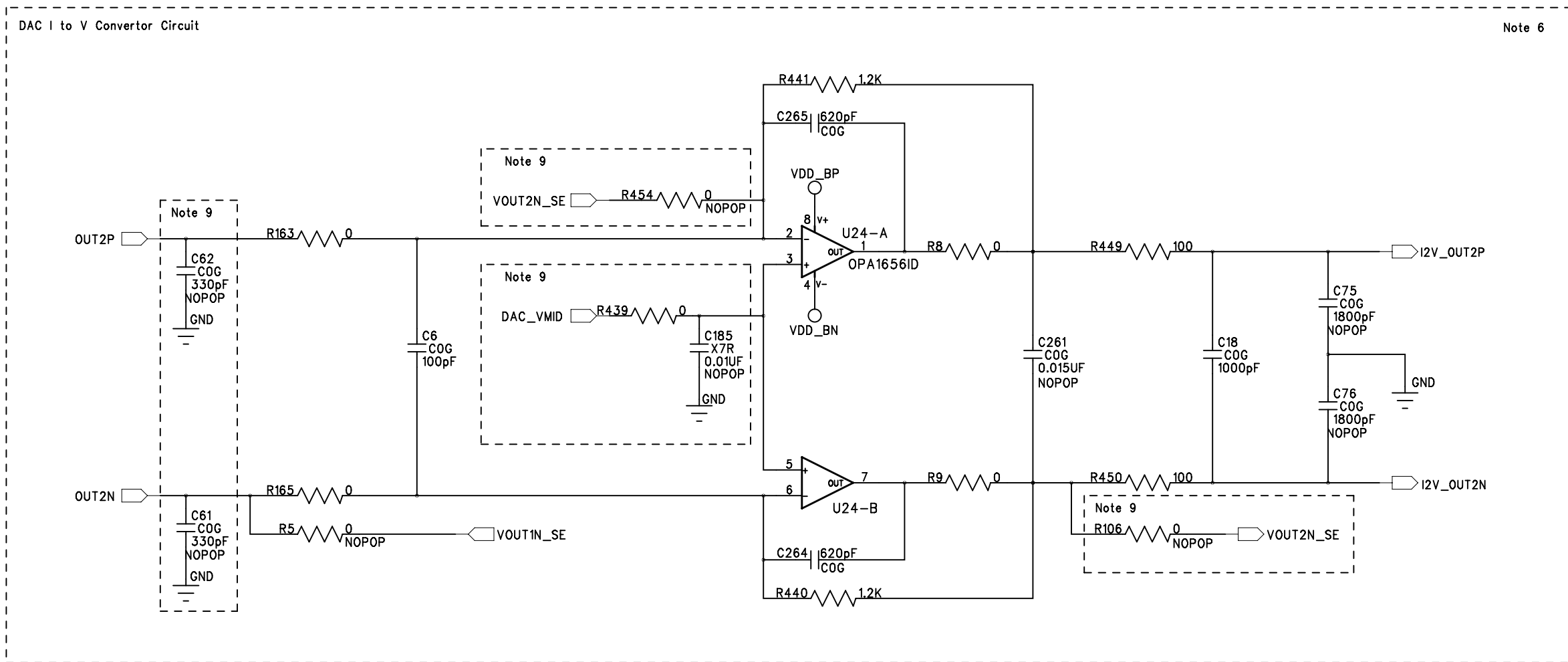
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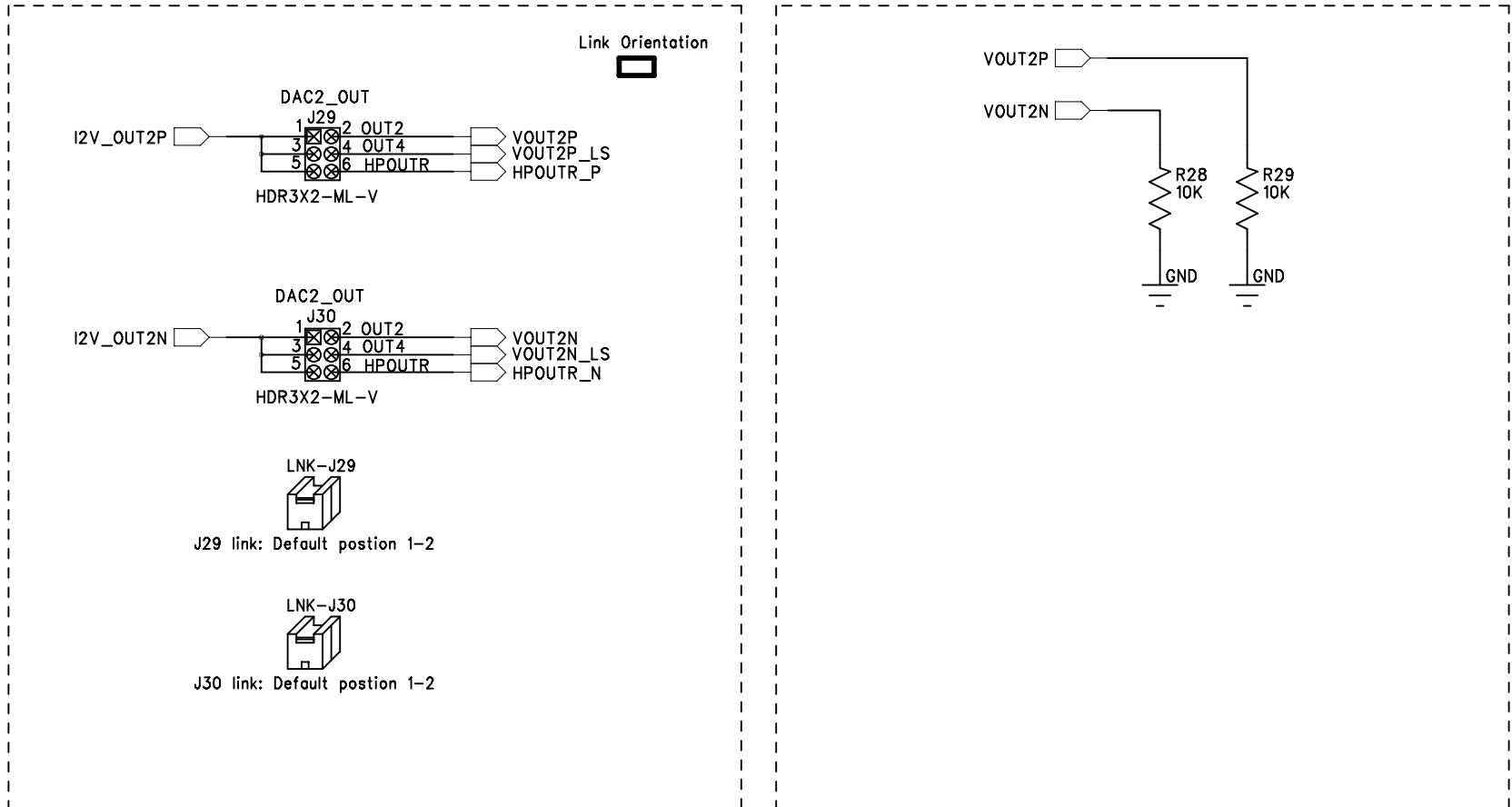
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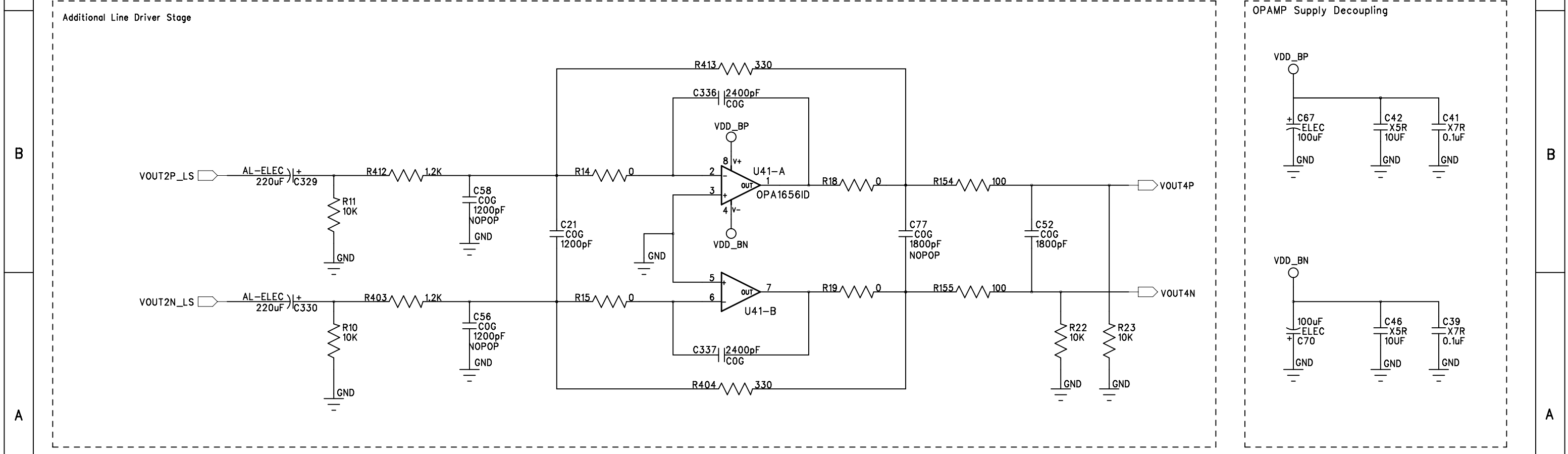
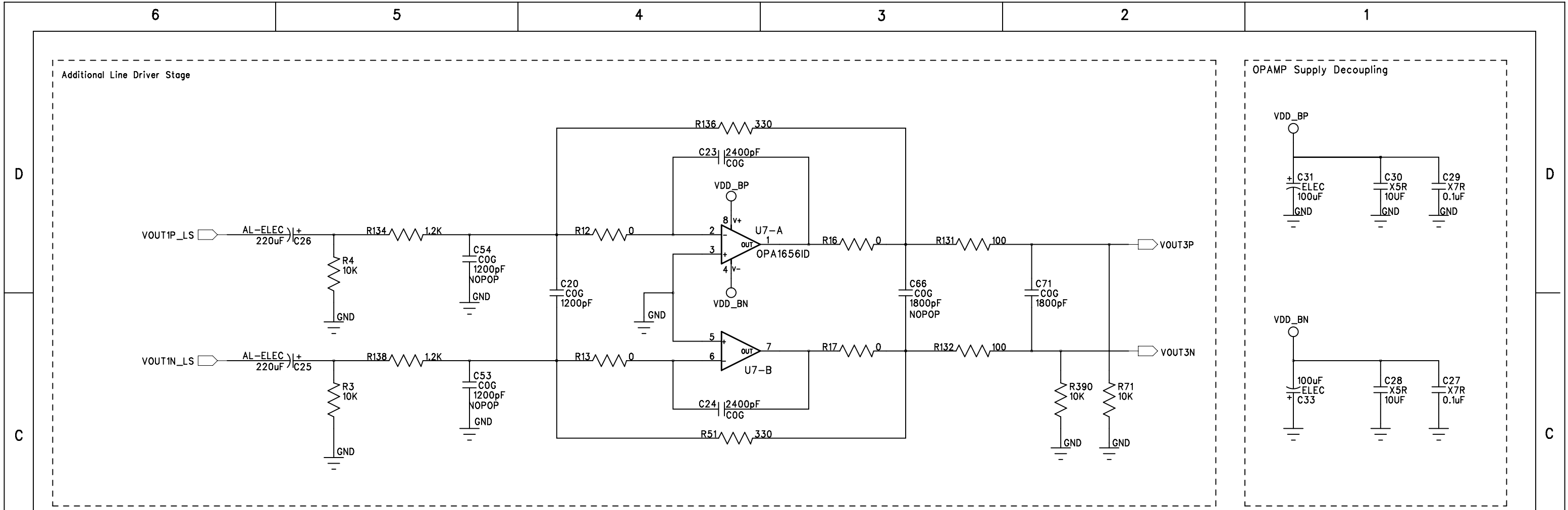
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Layout Notes

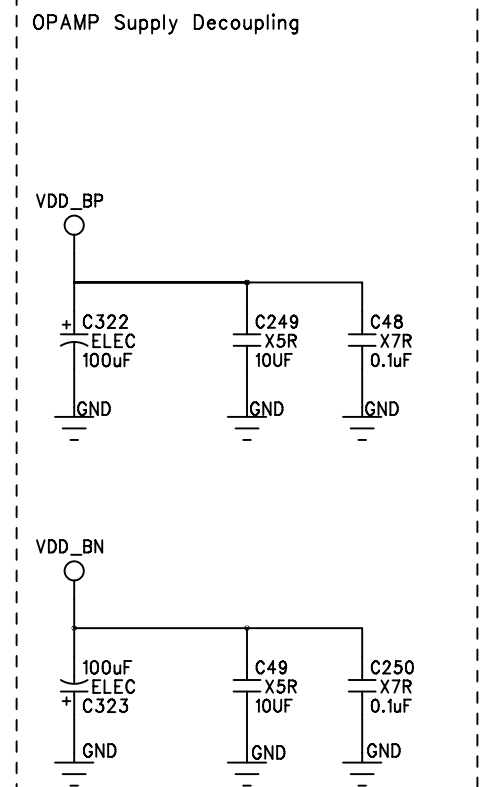
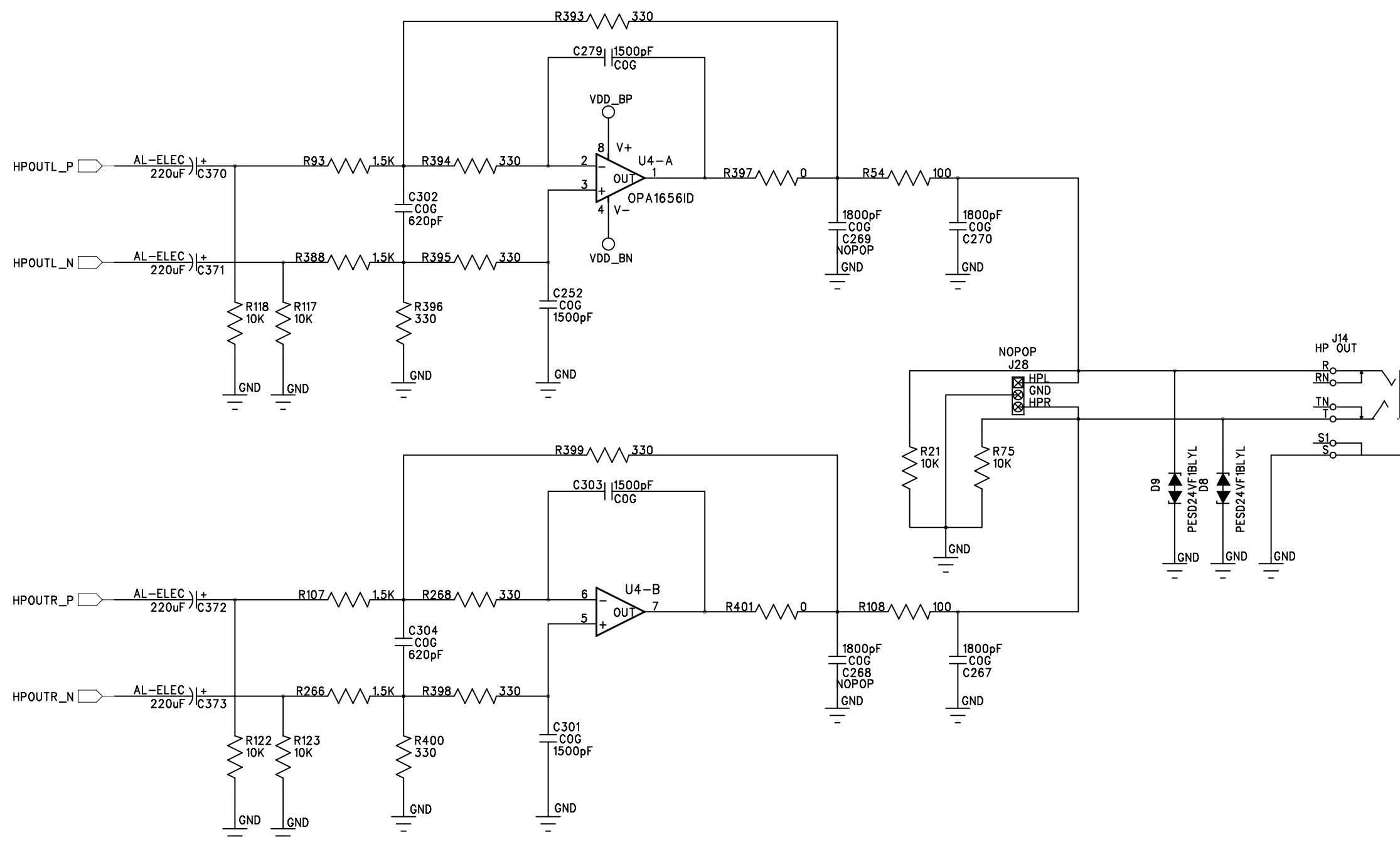
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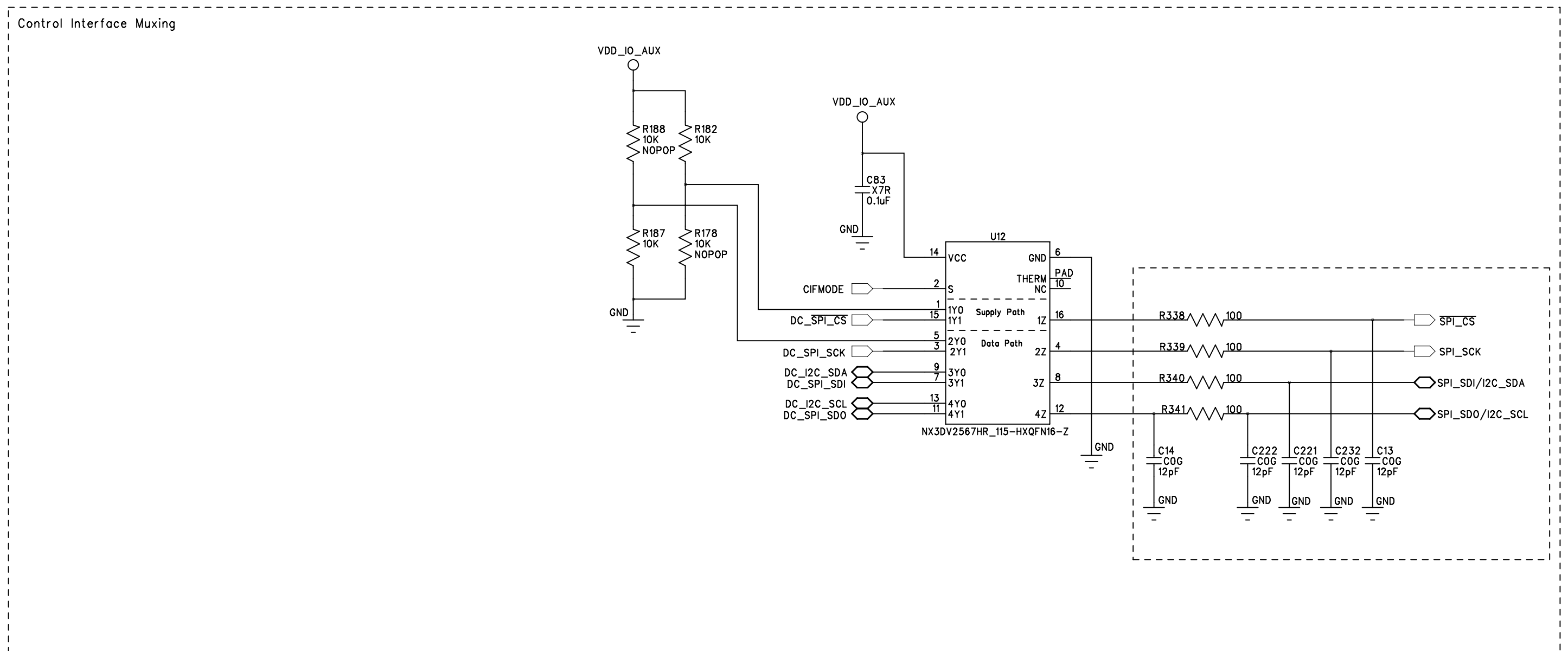
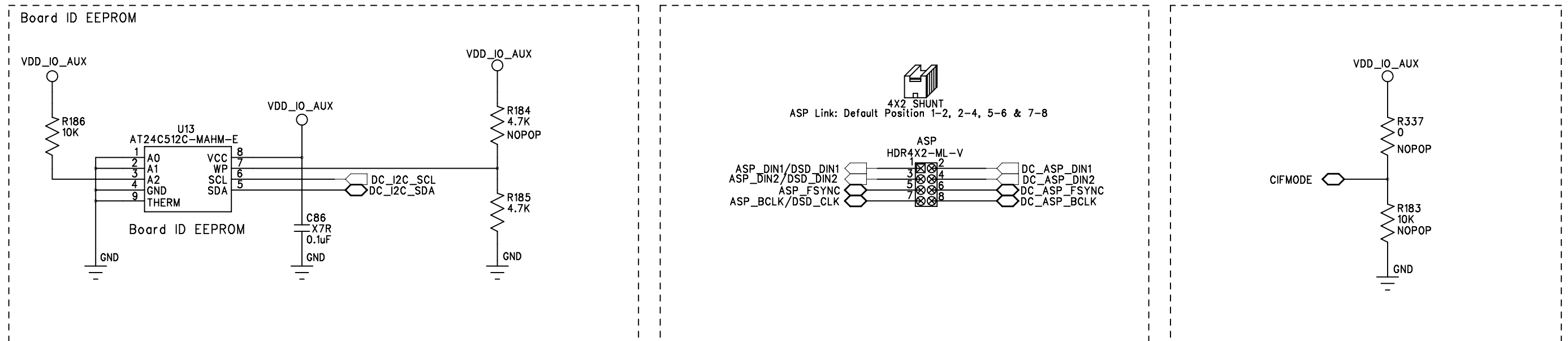
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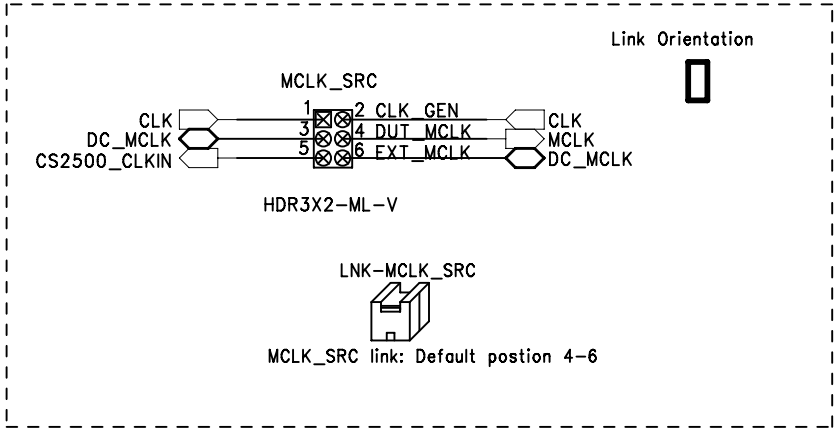
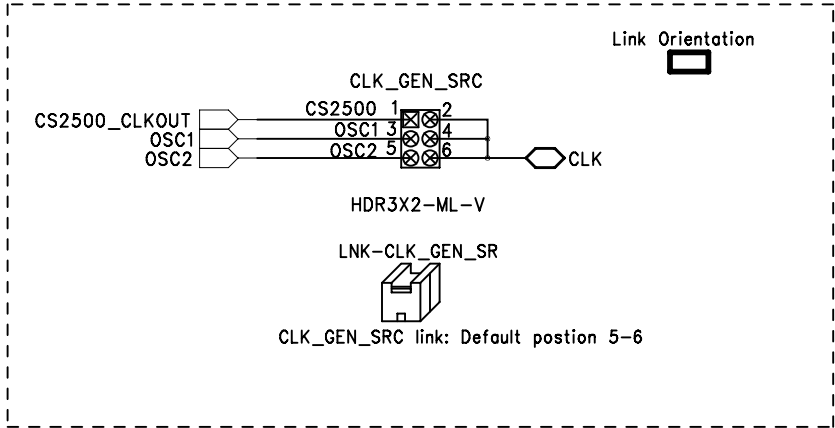
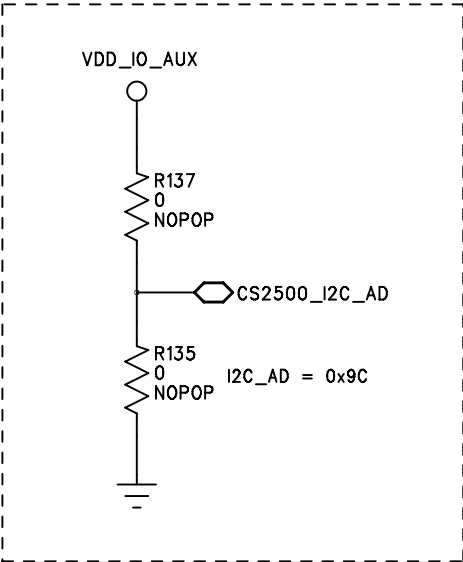


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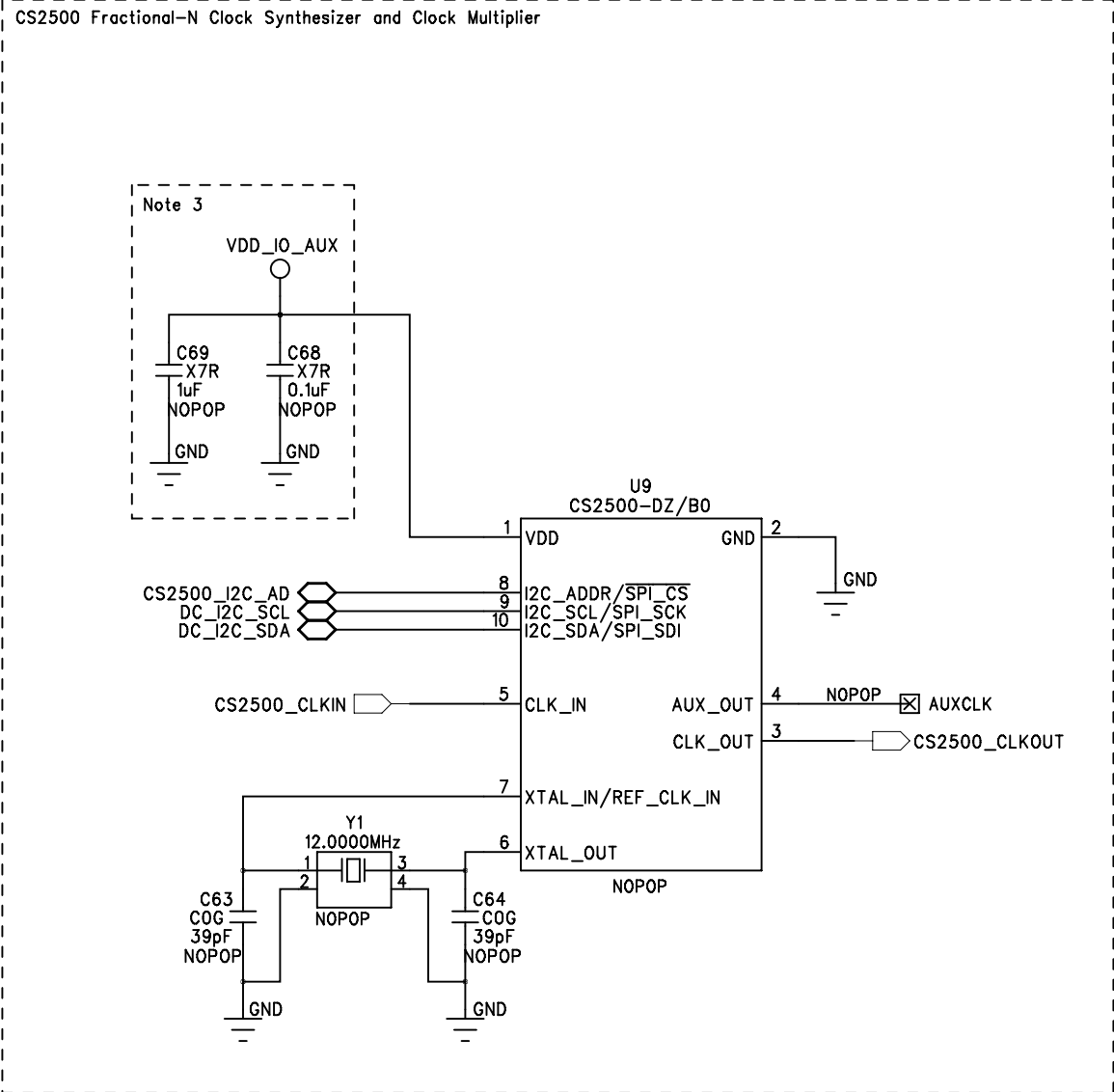




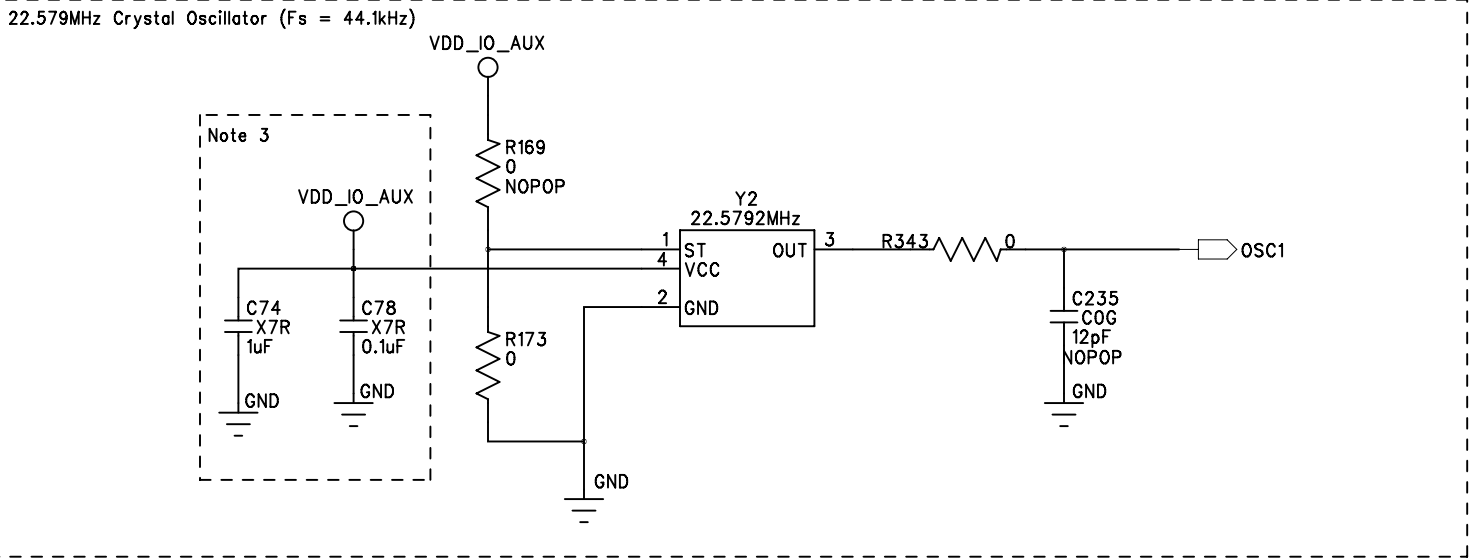
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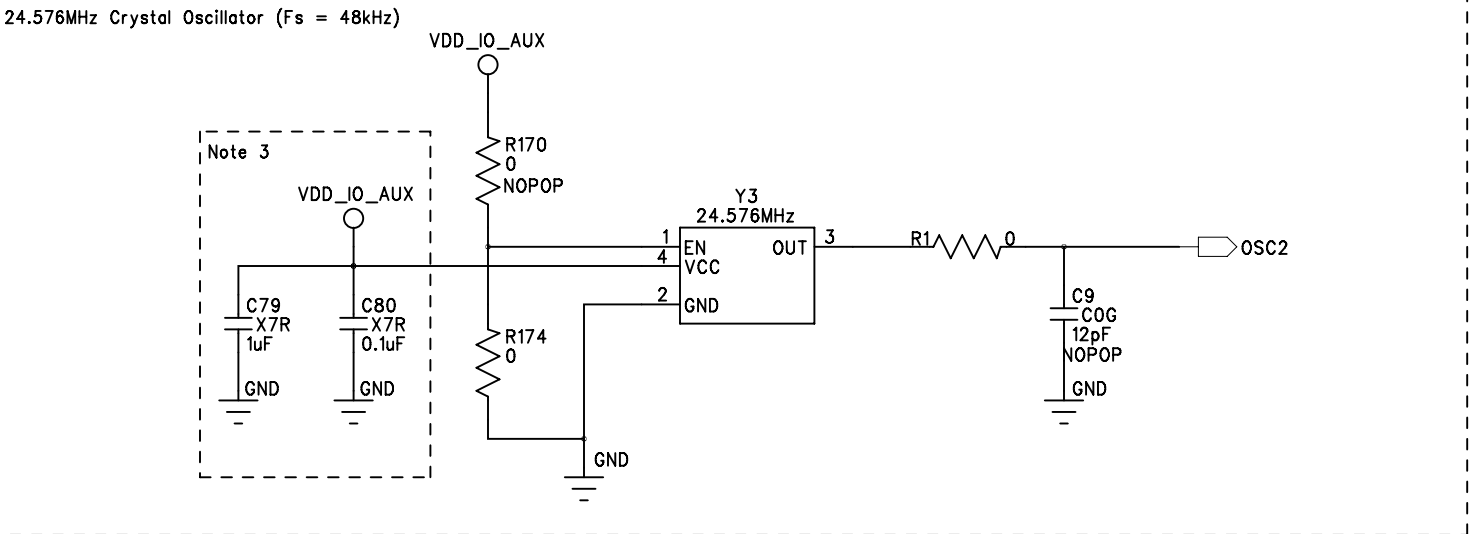
CS2500 Fractional-N Clock Synthesizer and Clock Multiplier



22.579MHz Crystal Oscillator (Fs = 44.1kHz)



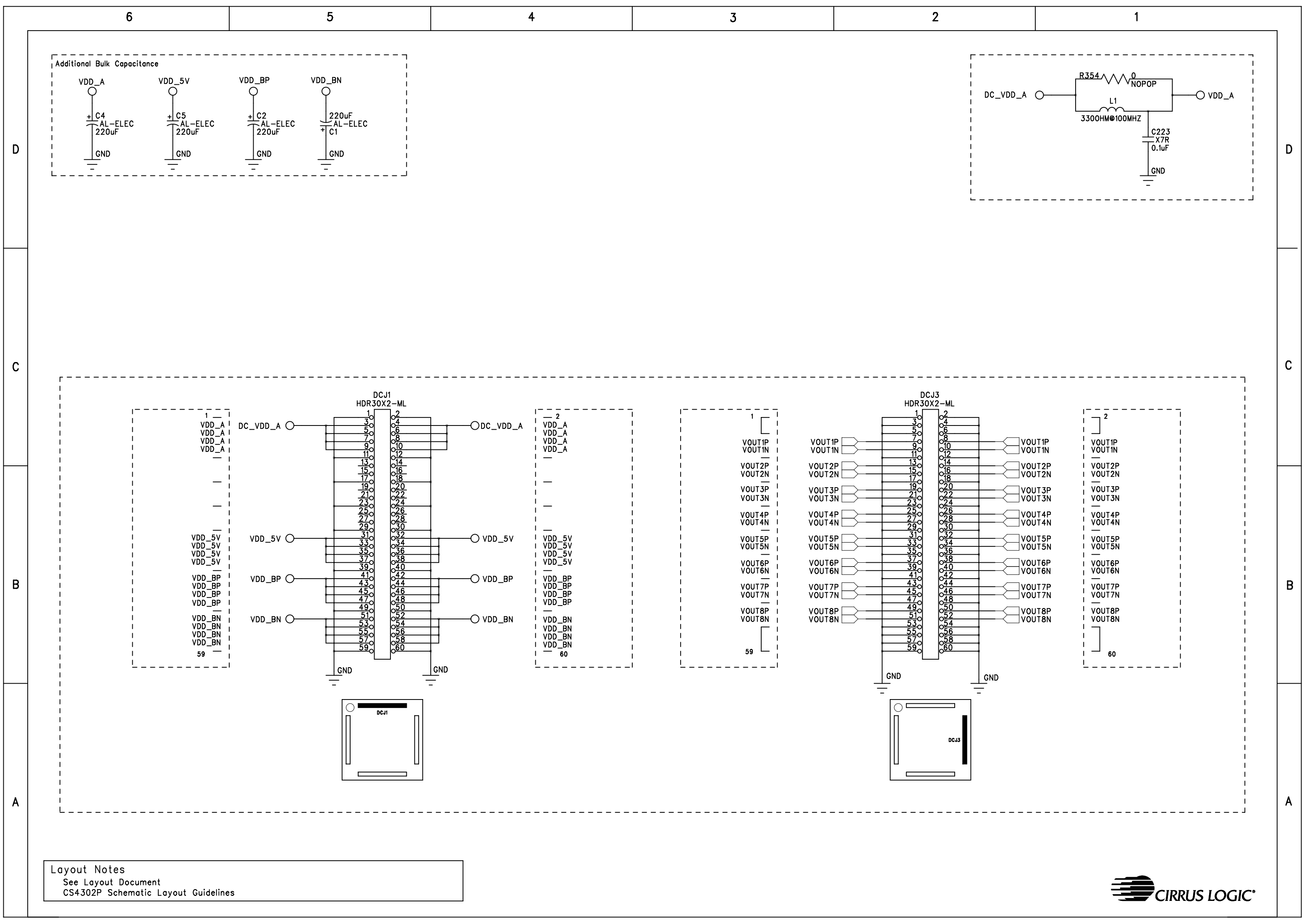
24.576MHz Crystal Oscillator (Fs = 48kHz)

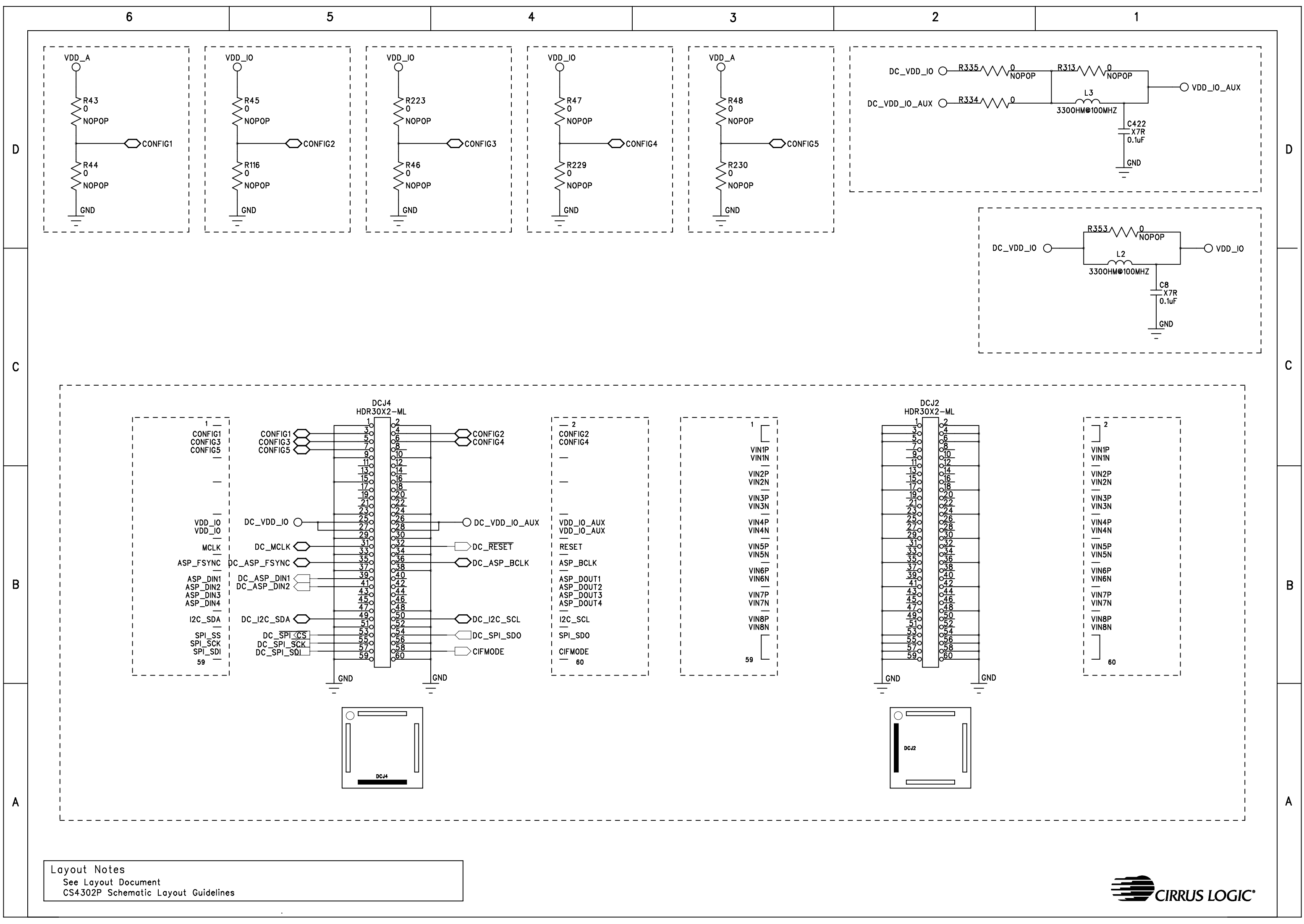


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