

DC4304P/4S/8P/8S-DAC Schematic and Layout

Introduction

The **DC4304P_4S_8P_8S-DAC Schematic & Layout.zip** contains the daughtercard schematic and layout information for the DC430x-DAC customer evaluation board with CS4304P, CS4304S, CS4308P and CS4308S silicon, as well as a customer reference.

Table 1 Board Information

Board Name	DC4304P-DAC	DC4304S-DAC	DC4308P-DAC	DC4308S-DAC
Board Type	Daughter Card	Daughter Card	Daughter Card	Daughter Card
Device	CS4304P	CS4304S	CS4308P	CS4308S
No of DACs	4	4	8	8

1 Folder Contents

The contents of the **DC4304P_4S_8P_8S-DAC Schematic & Layout.zip** is detailed in Table 2.

Table 2 DC4304P/4S/8P/8S-DAC Schematic & Layout.zip Contents

PCB Schematics	DC4304P-DAC-SCH-REV-A1.pdf DC4304S-DAC-SCH-REV-A0.pdf DC4308P-DAC-SCH-REV-A0.pdf DC4308S-DAC-SCH-REV-A3.pdf
PCB Layout	DC430X-DAC-PCB-REV-A0.pdf
PCB Schematic & Layout Guidelines	CS430## Schematic Layout Guidelines.pdf
PCB Layout Gebers	DC430X-DAC-GERBER-REV-A0.zip
PCB Bill of Materials	DC4304P-DAC-BOM-REV-A0.pdf DC4304S-DAC-BOM-REV-A0.pdf DC4308P-DAC-BOM-REV-A0.pdf DC4308S-DAC-BOM-REV-A0.pdf

2 Revision History

Revision History	
Revision	Changes
1.0 JULY 2025	• Initial version.
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Contacting Cirrus Logic Support

For all product questions and inquiries, contact a Cirrus Logic Sales Representative.

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