
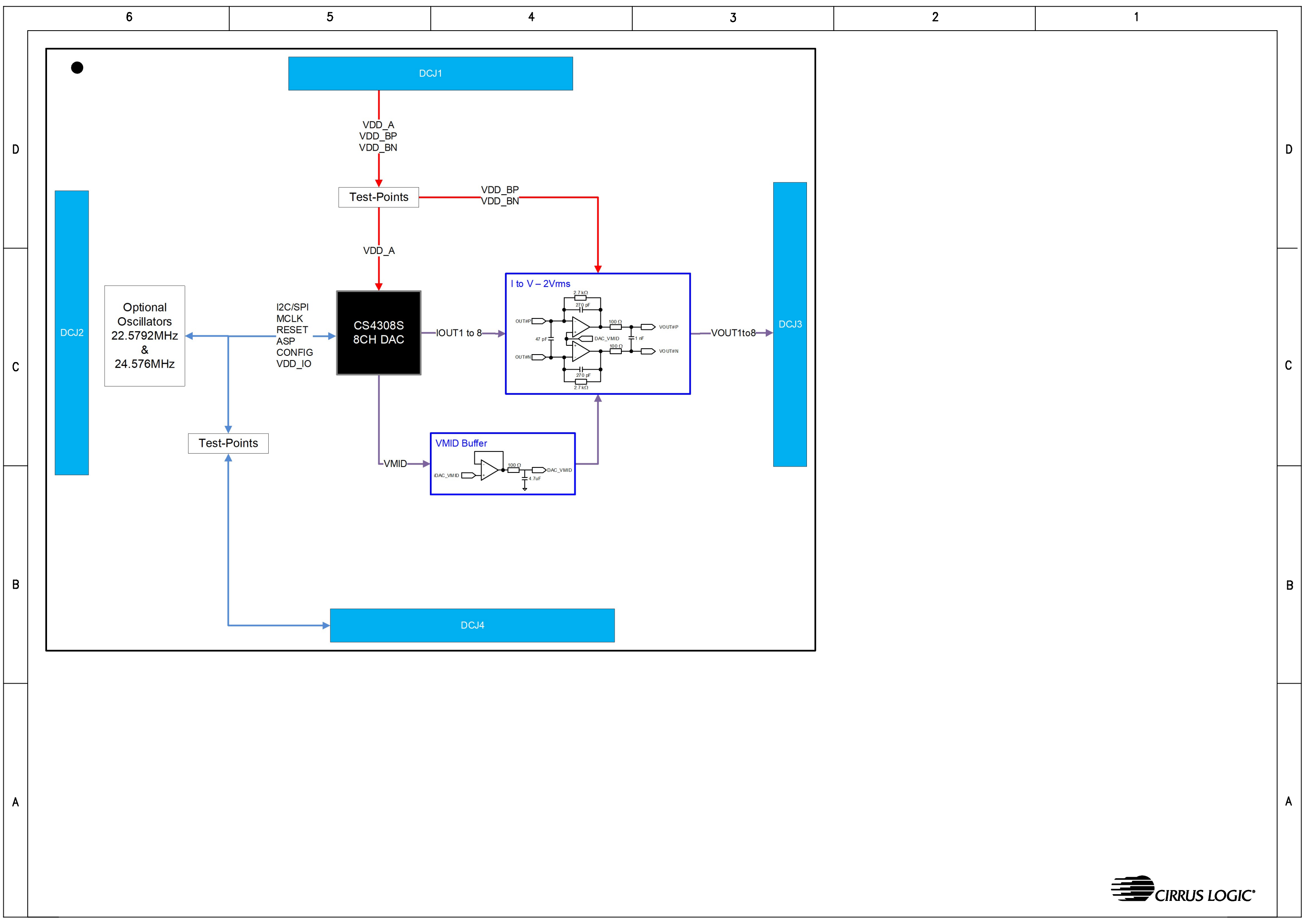
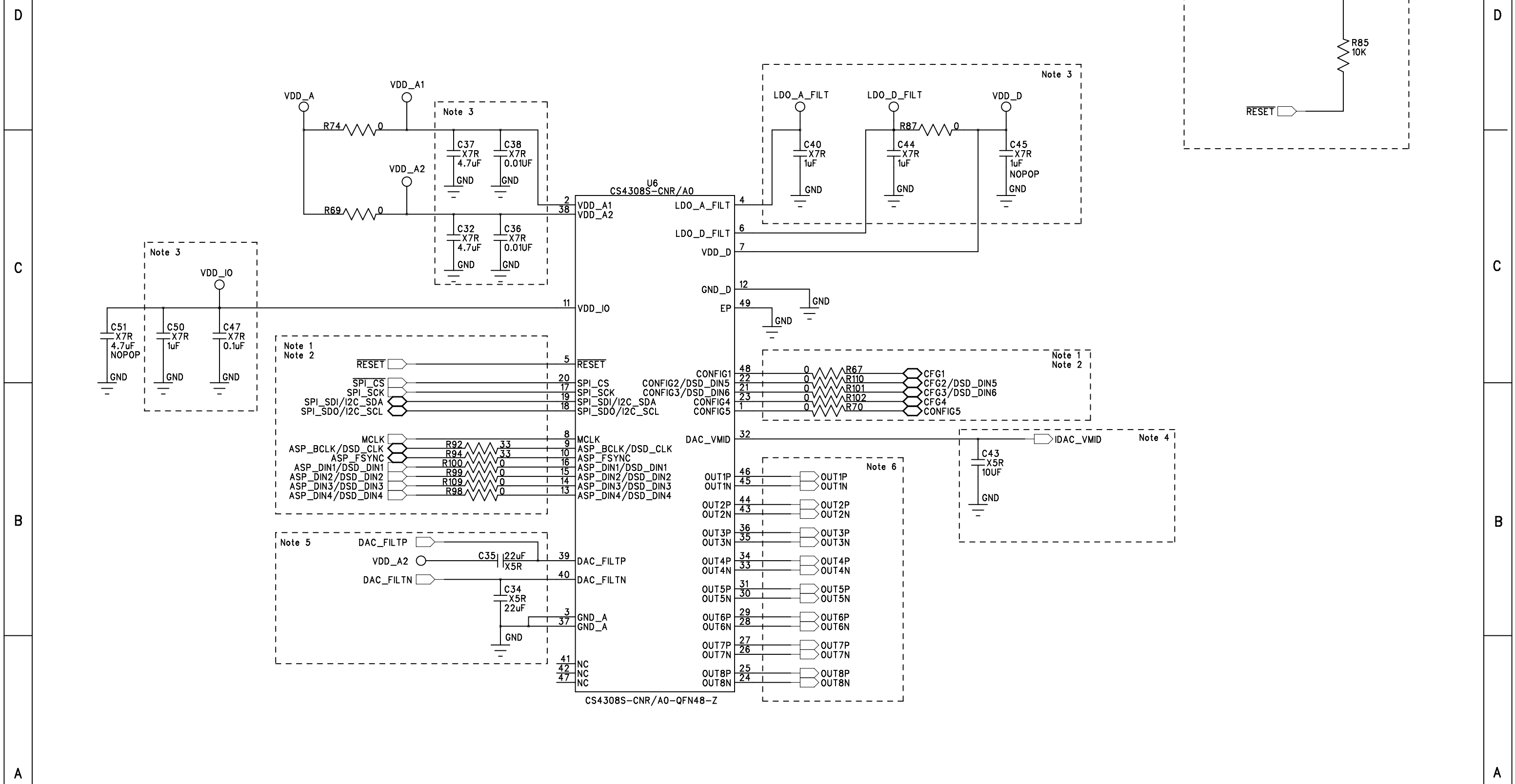
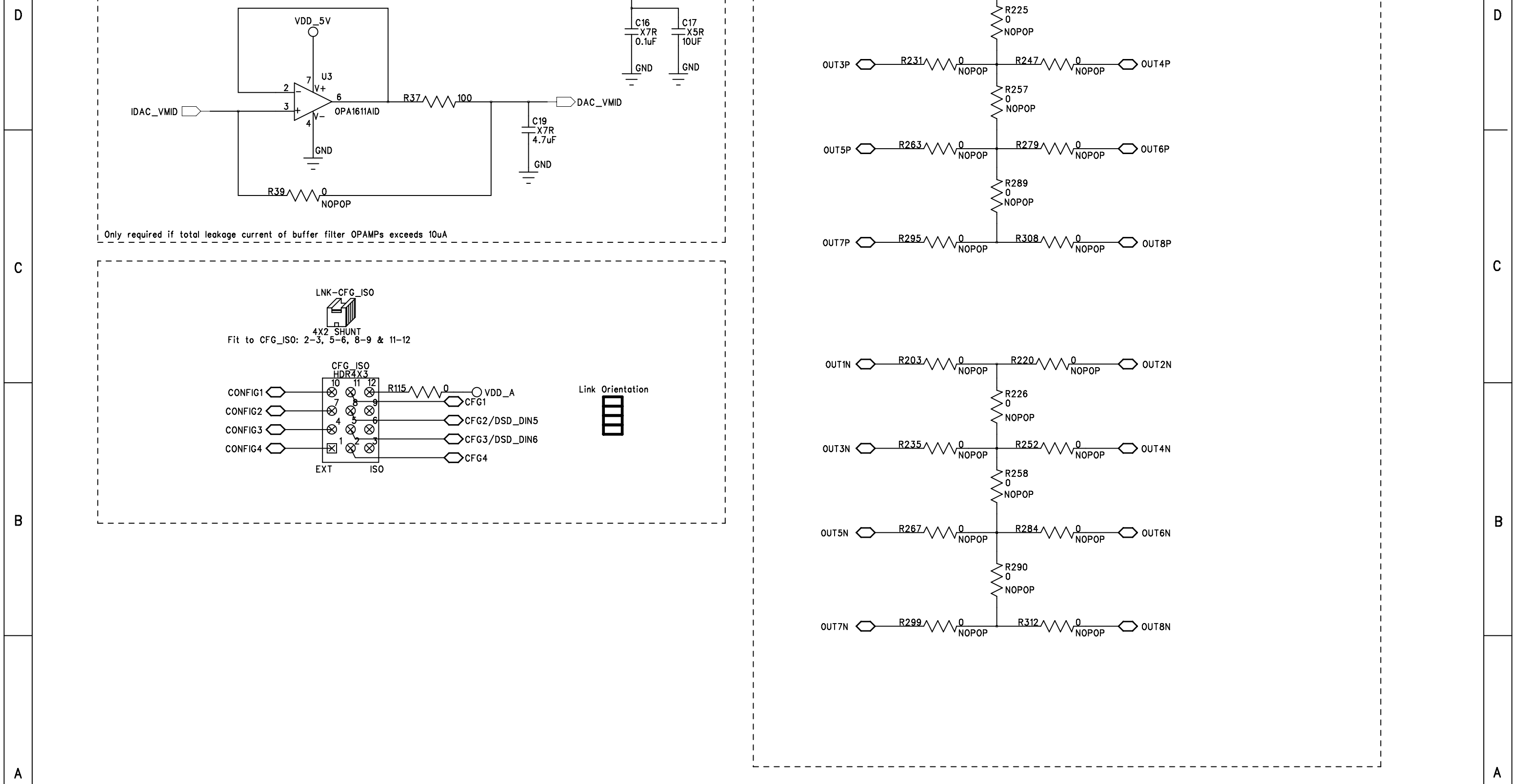


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D	CS4308S_DAC		CS4308S_MISC		CLK_GEN						
	SHEET-3		SHEET-4		SHEET-15						
	OUTPUT_BUFFER_CONFIG		ANALOG_OUT1		ANALOG_OUT2		ANALOG_OUT3		ANALOG_OUT4		
	SHEET-5		SHEET-6		SHEET-7		SHEET-8		SHEET-9		
	ANALOG_OUT5		ANALOG_OUT6		ANALOG_OUT7		ANALOG_OUT8				
C	SHEET-10		SHEET-11		SHEET-12		SHEET-13				
	DC_CONN1		DC_CONN2		MISC						
	SHEET-16		SHEET-17		SHEET-14						
	TEST_POINTS										
	SHEET-18										
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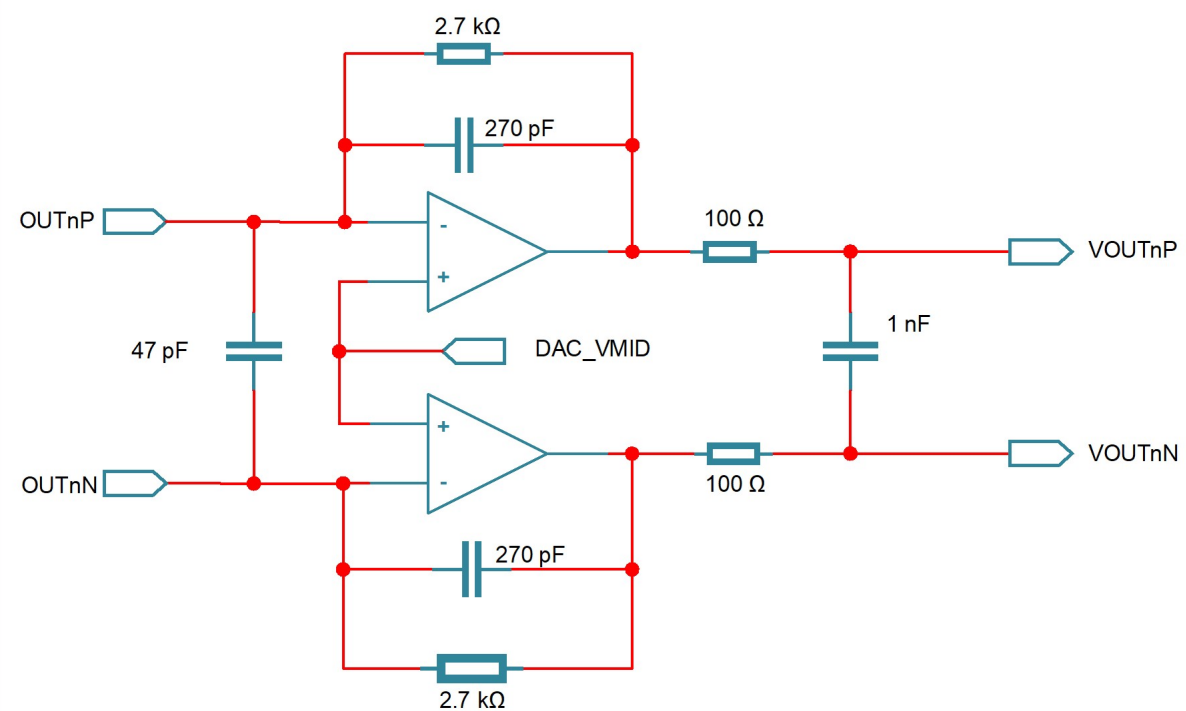




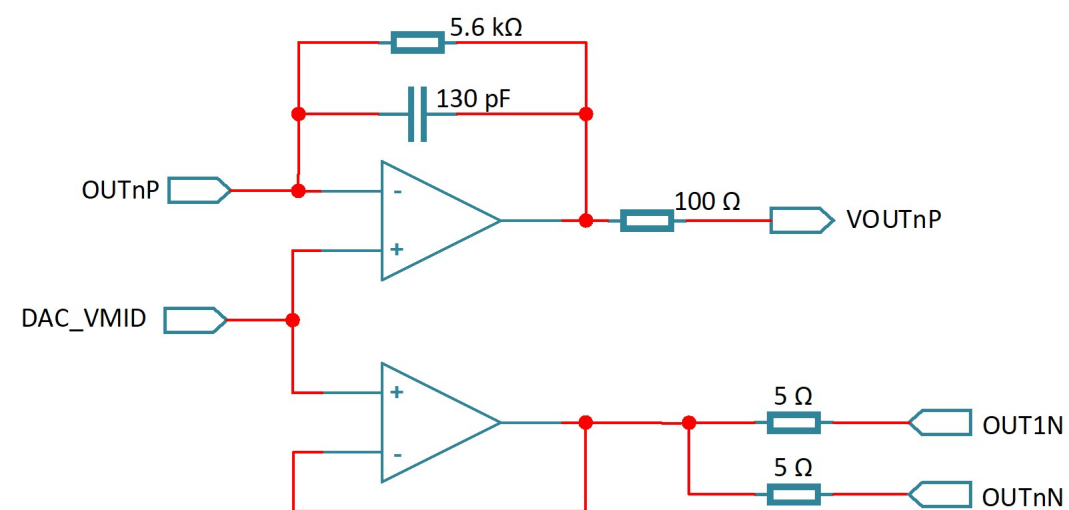


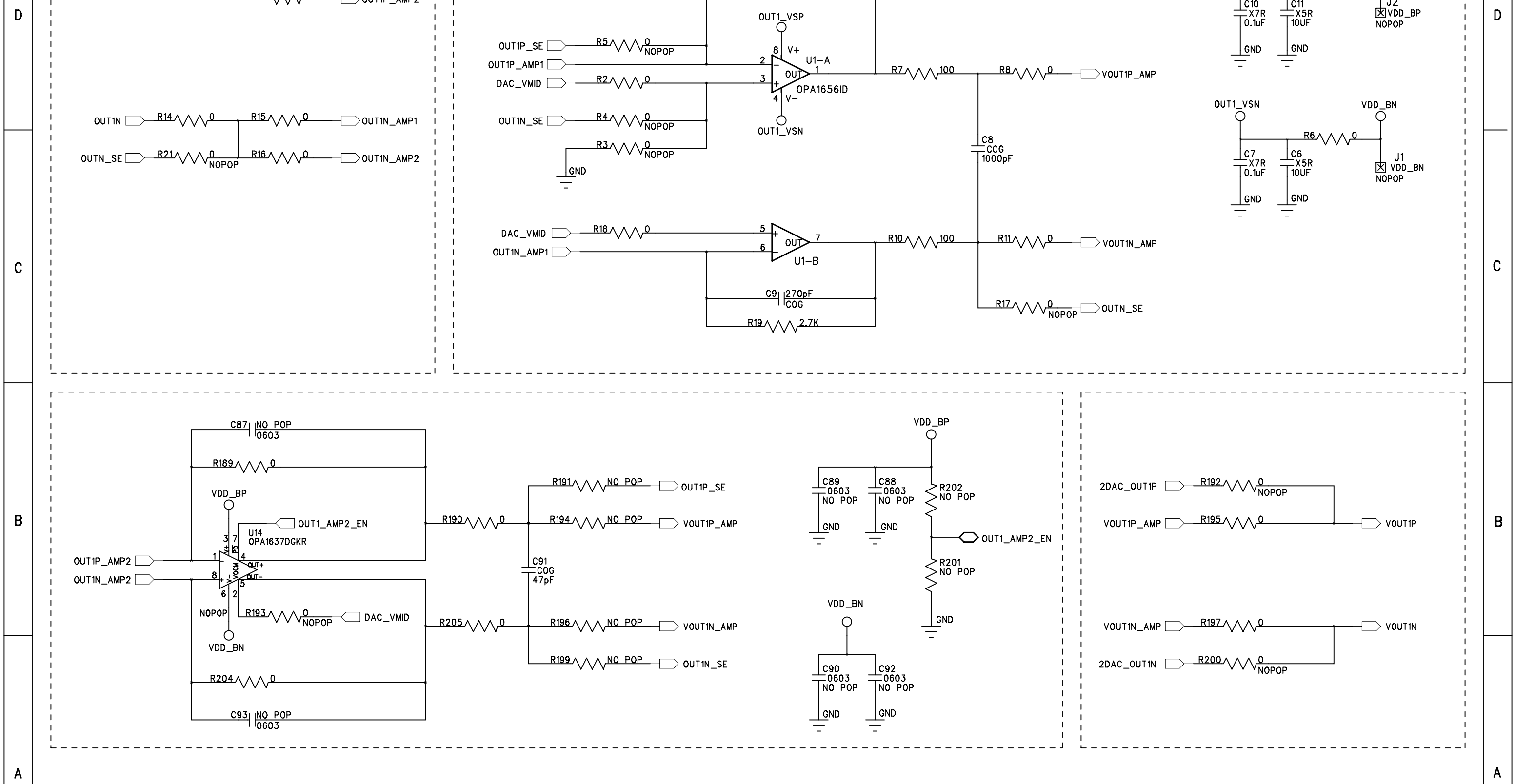
## Output I to V Circuits

### Differential Output – 2Vrms Output



### Single Ended Half Output – 2Vrms Output





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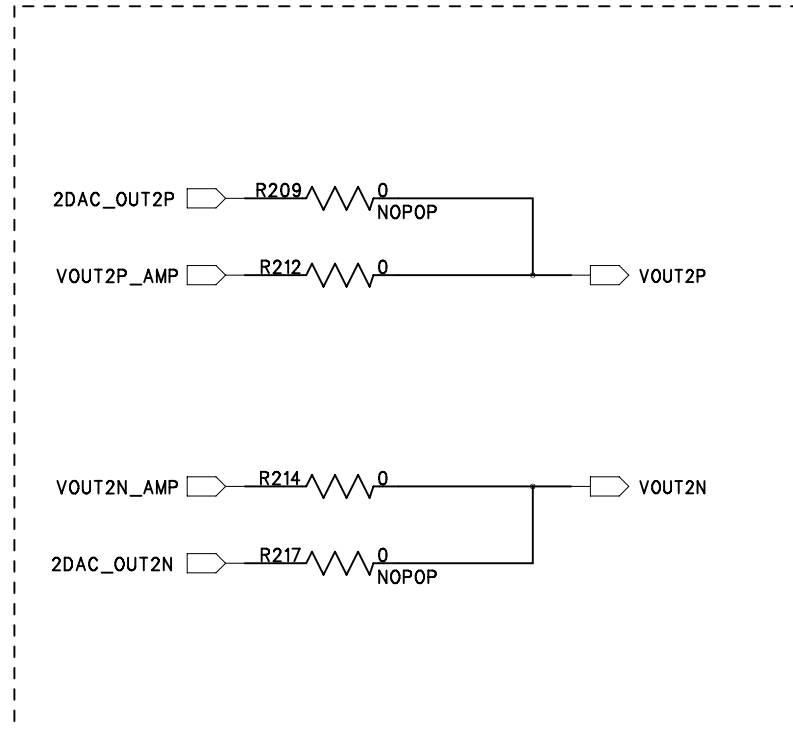
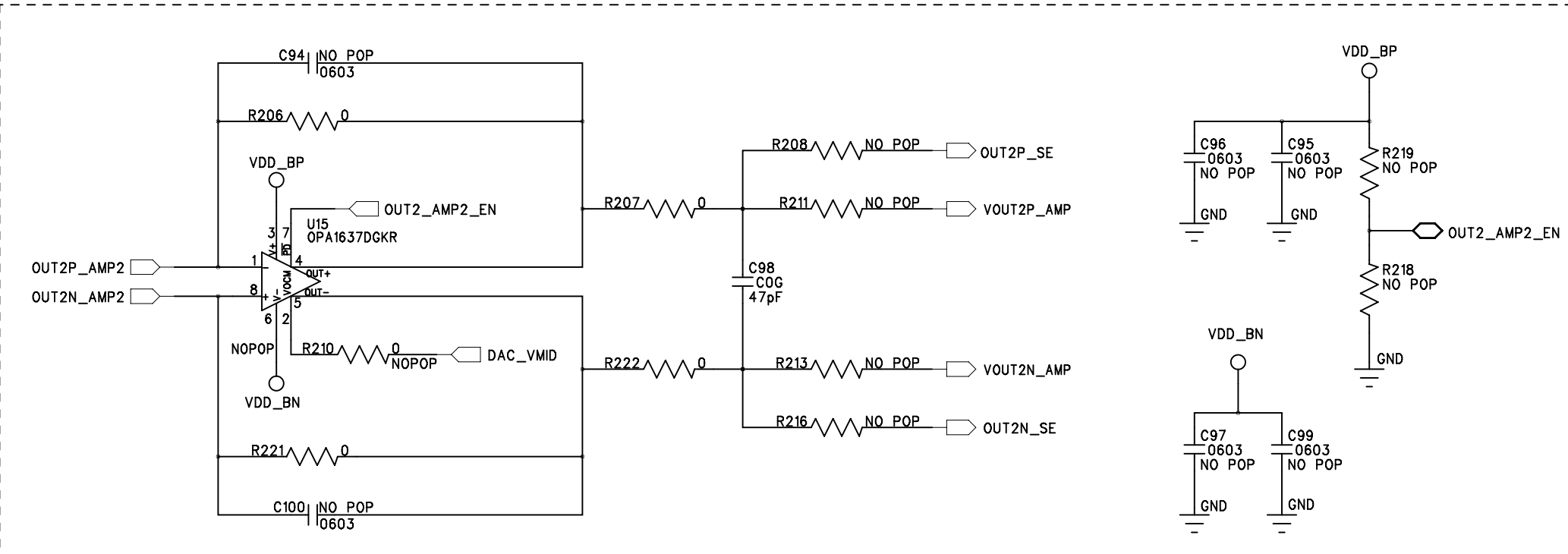
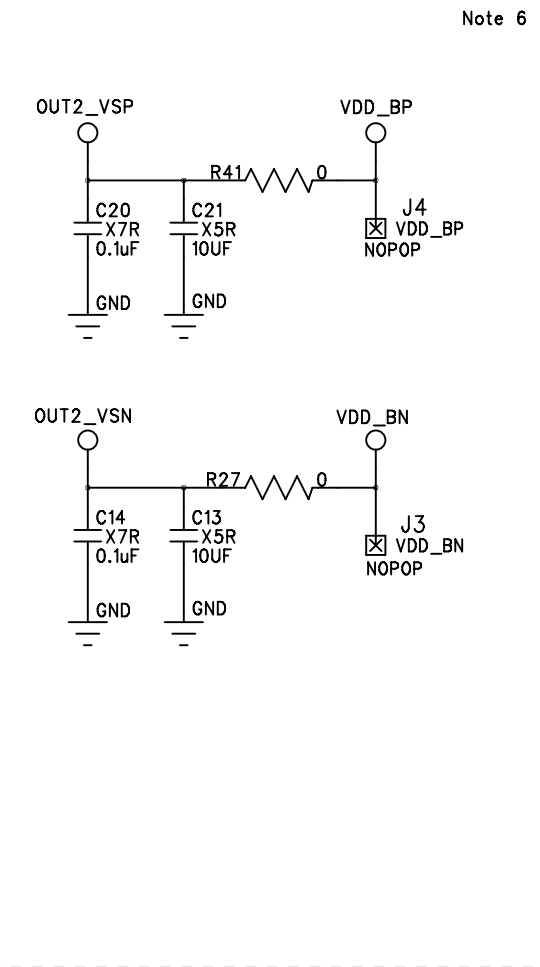
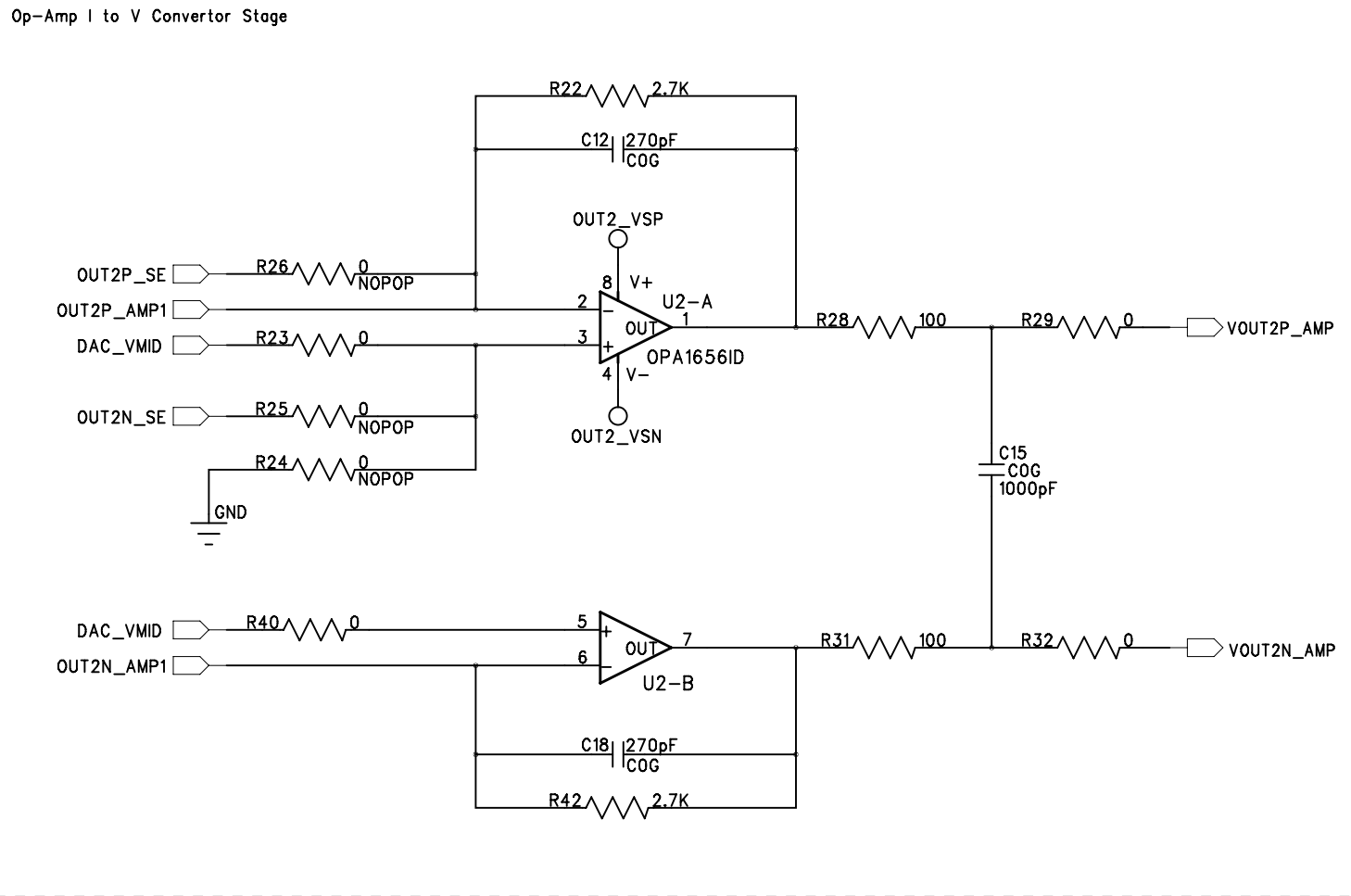
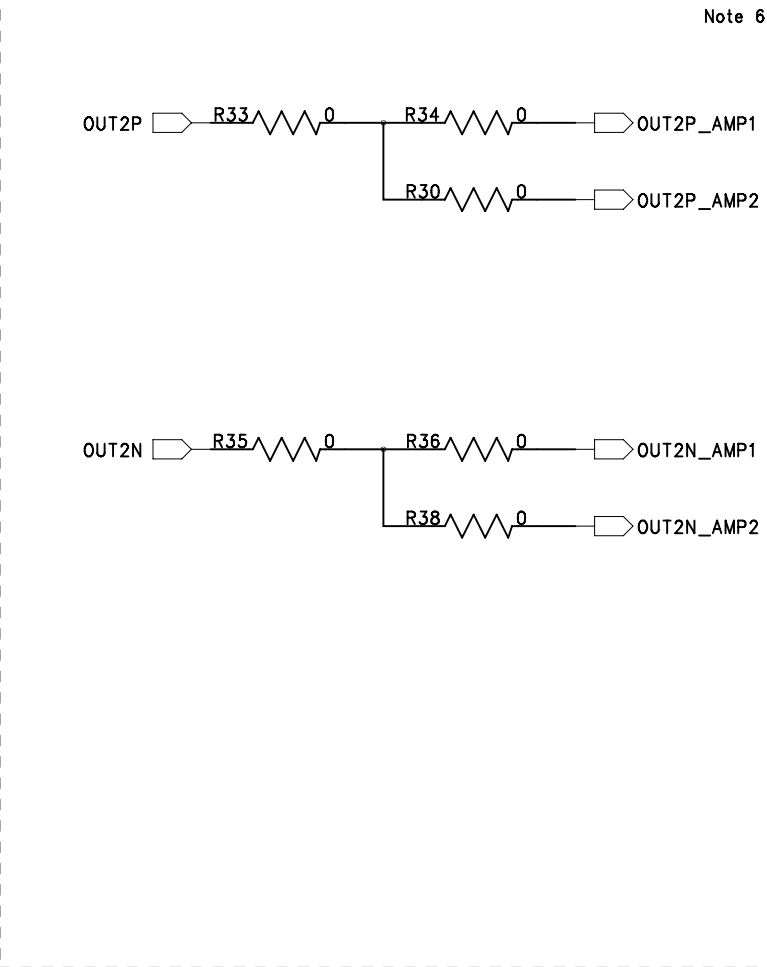
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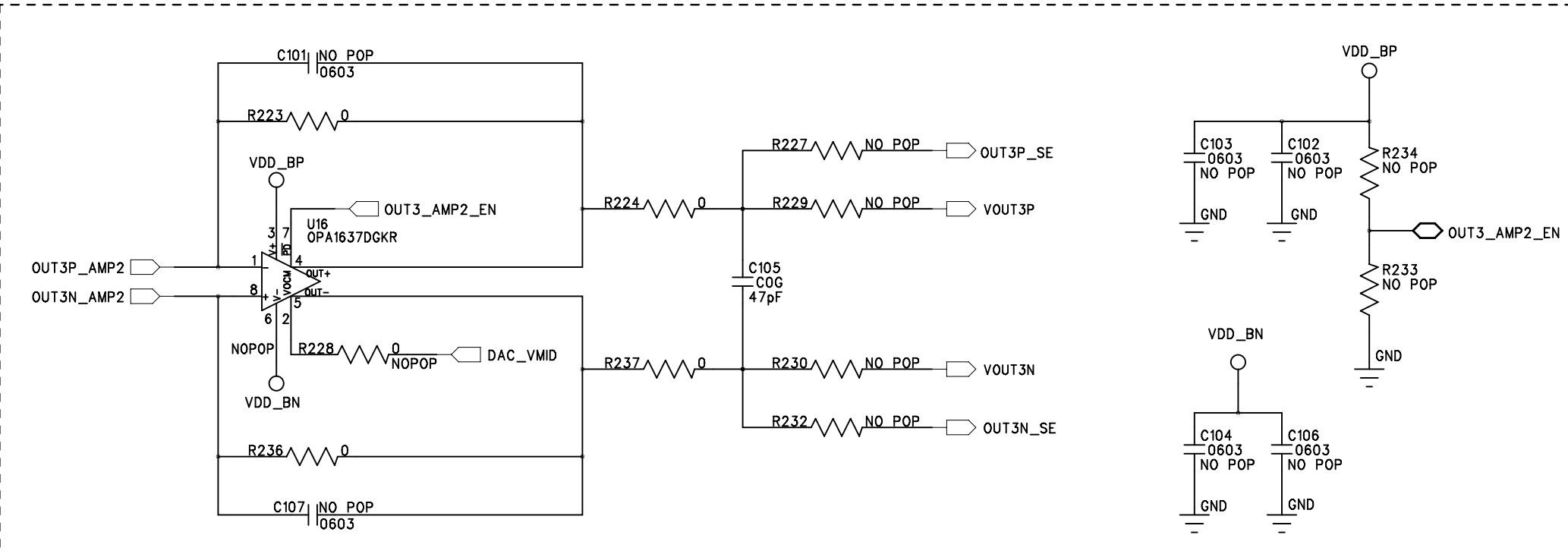
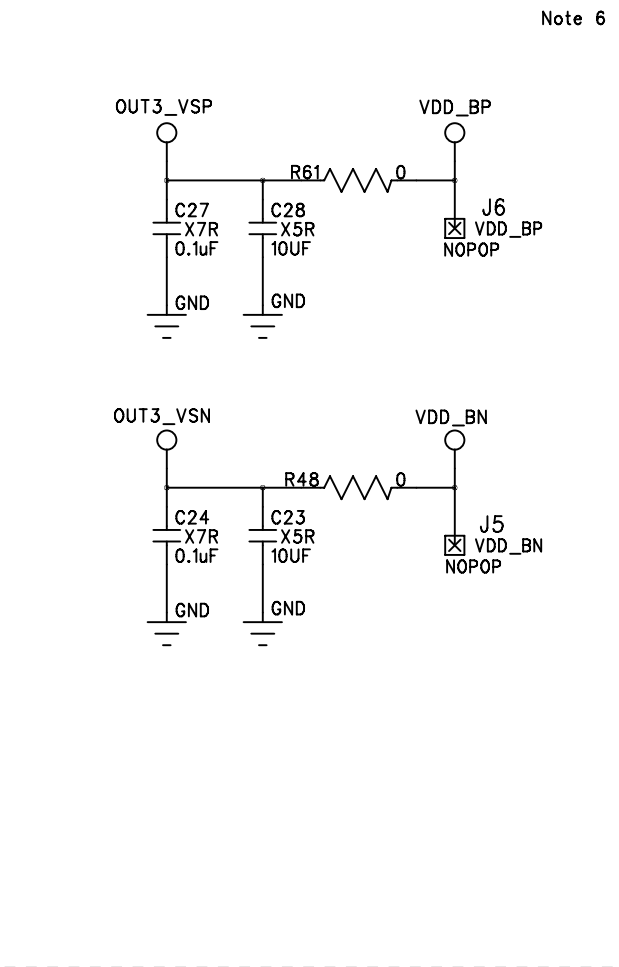
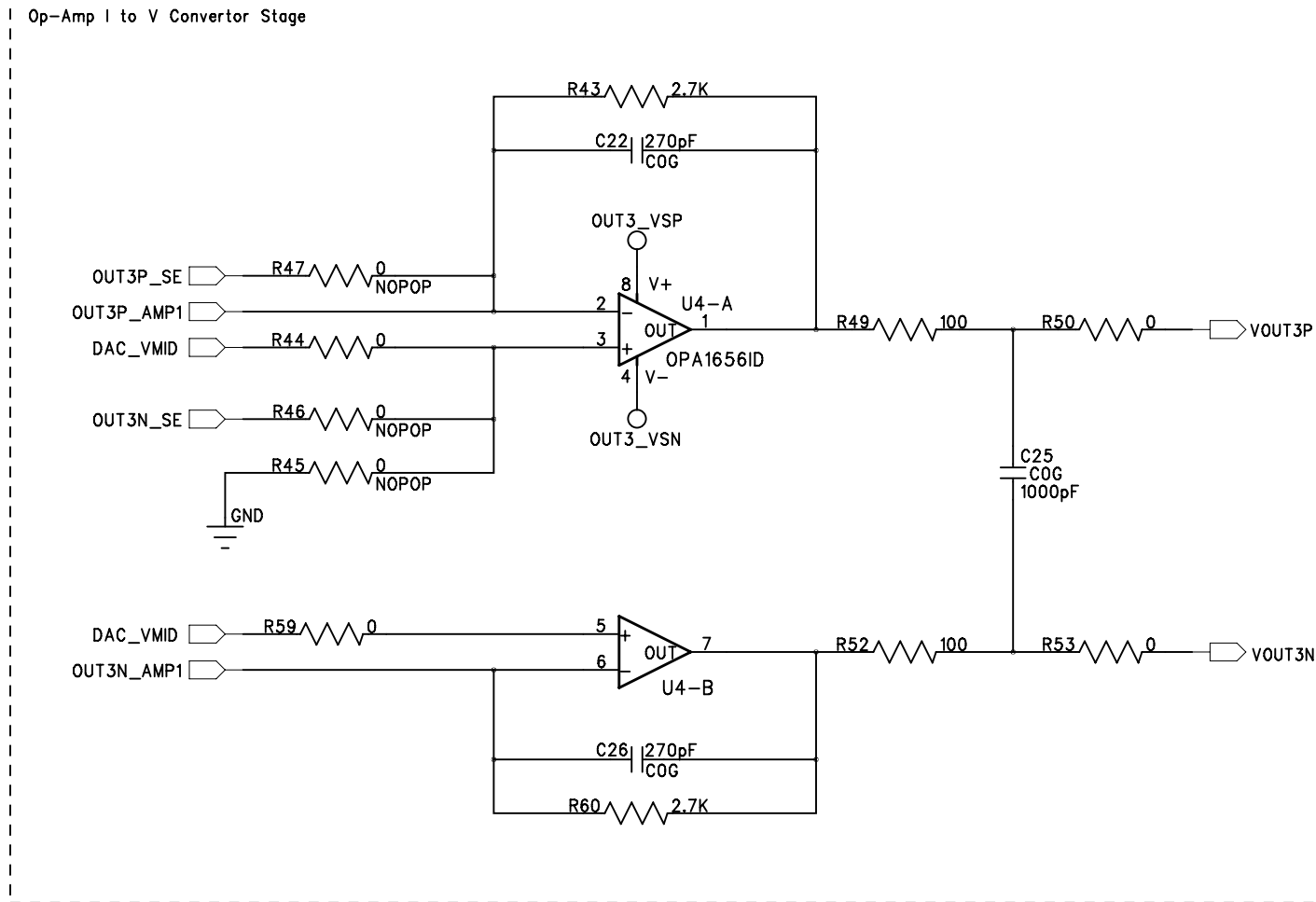
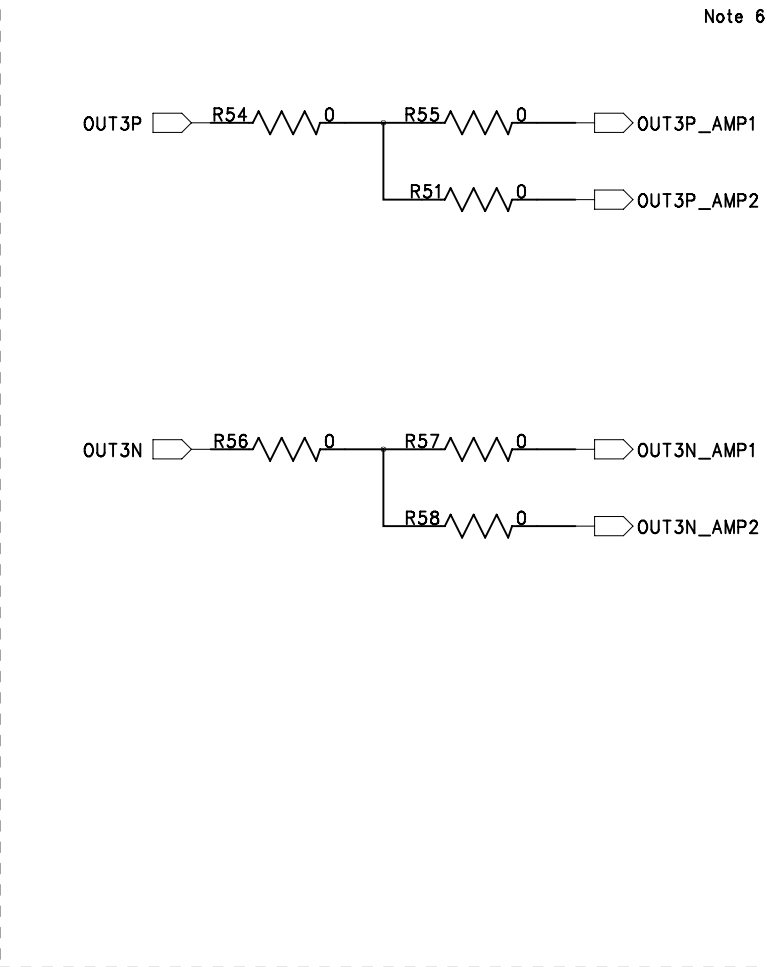
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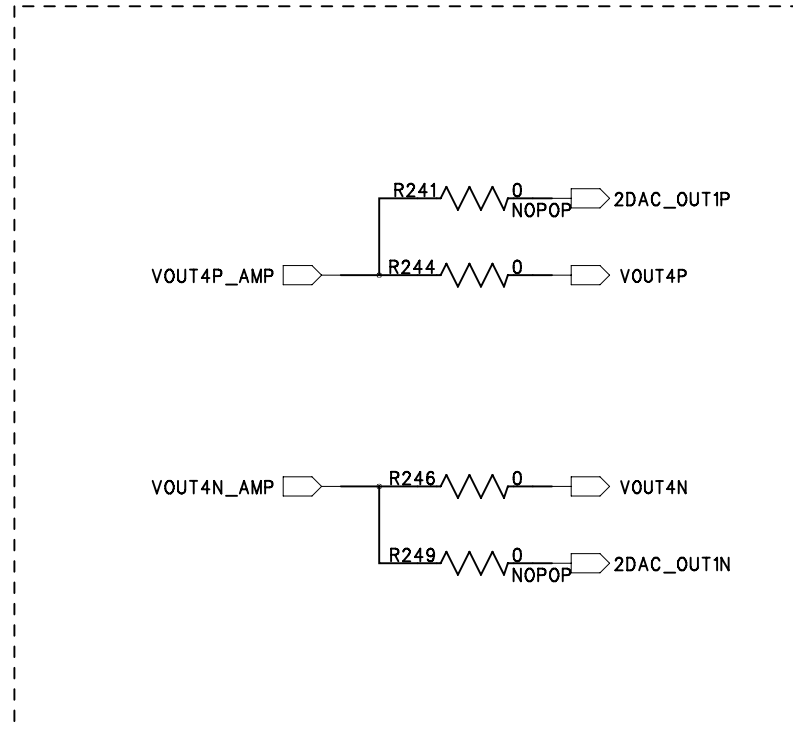
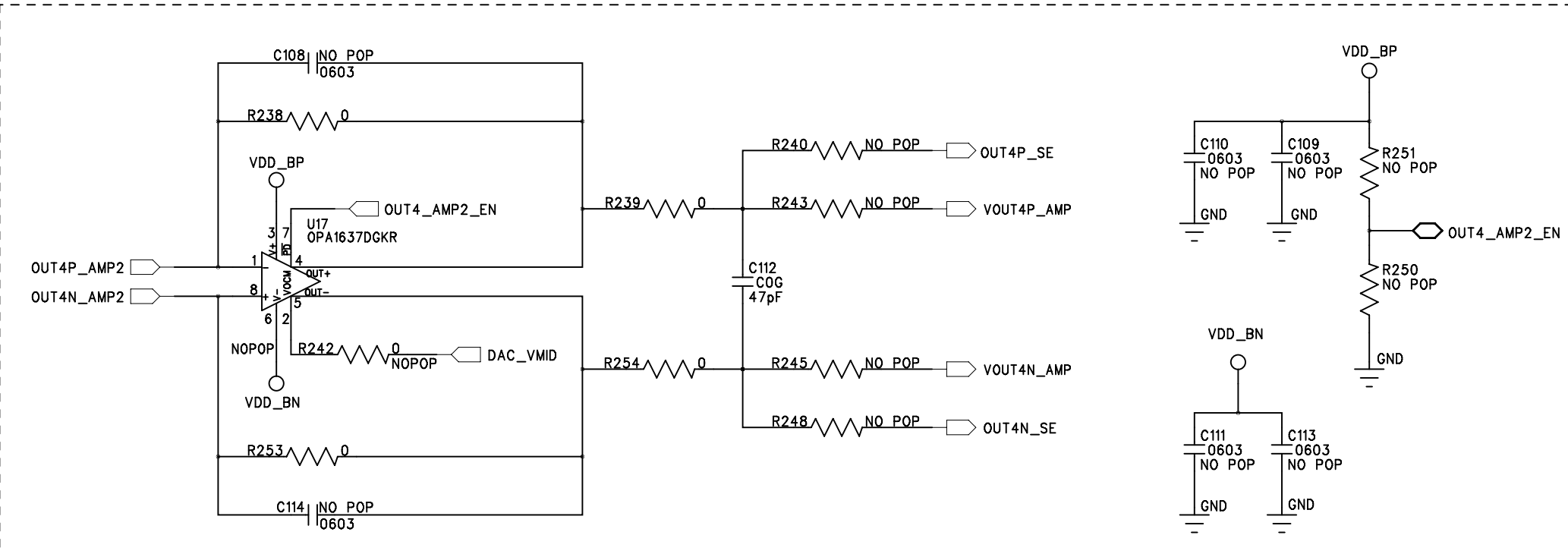
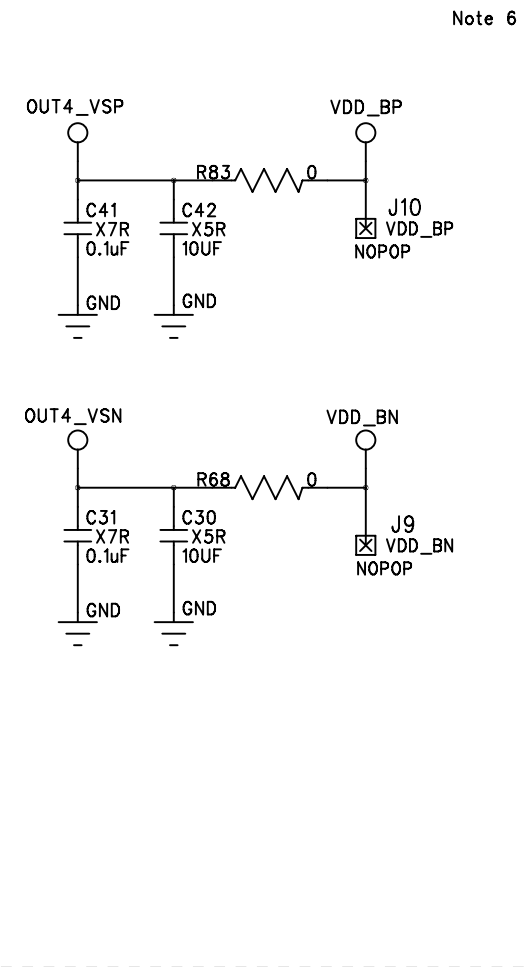
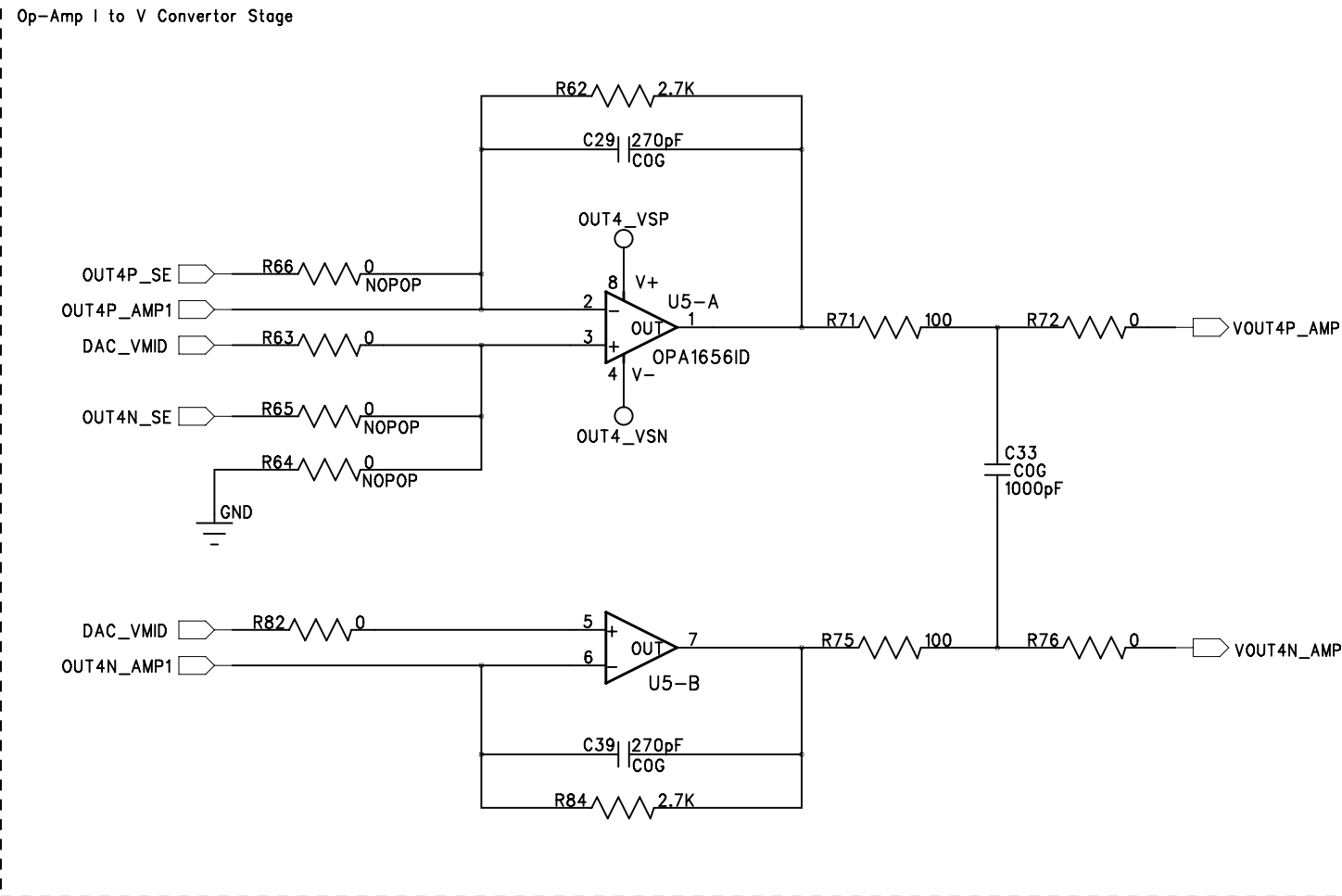
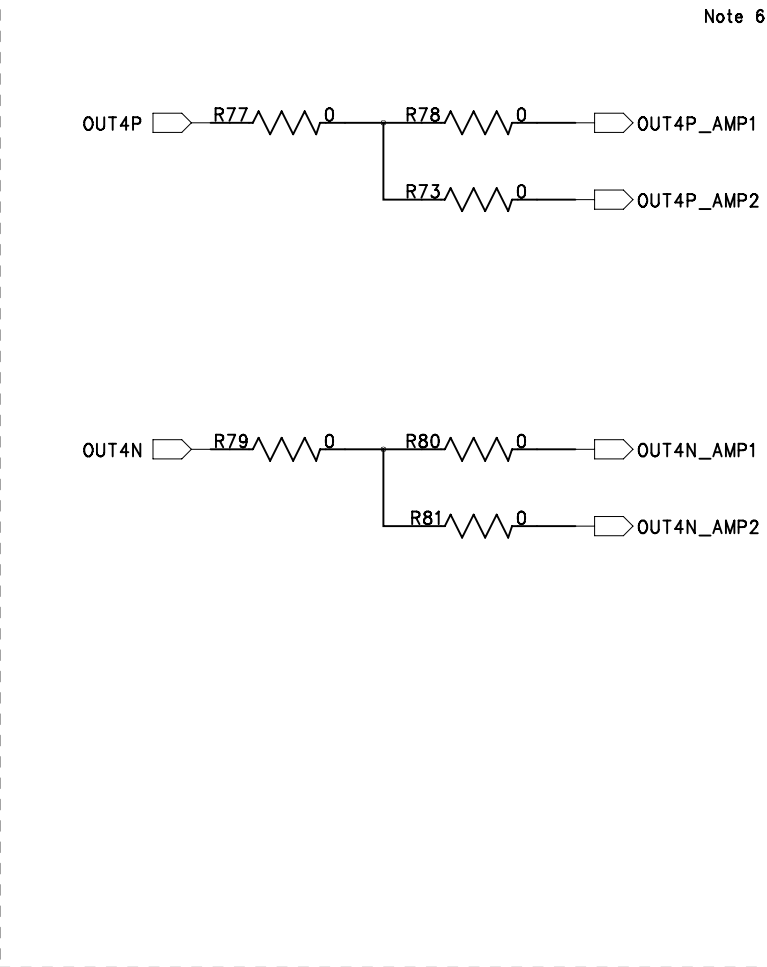
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CS430## Schematic Layout Guidelines



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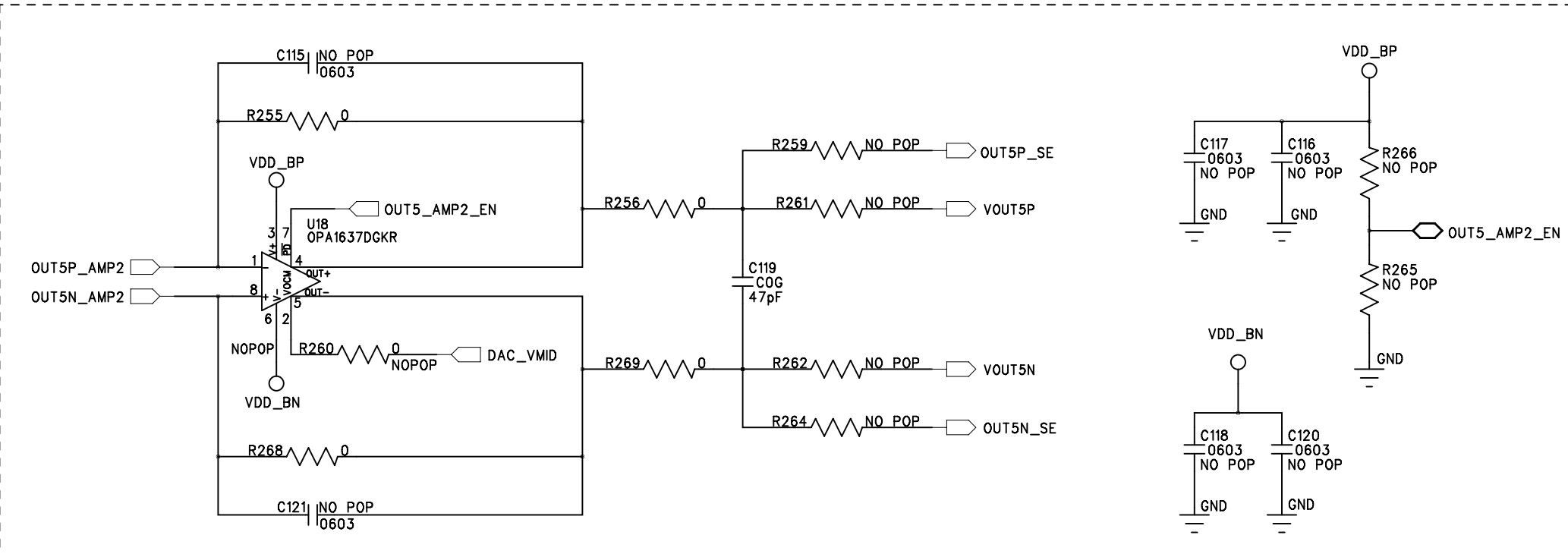
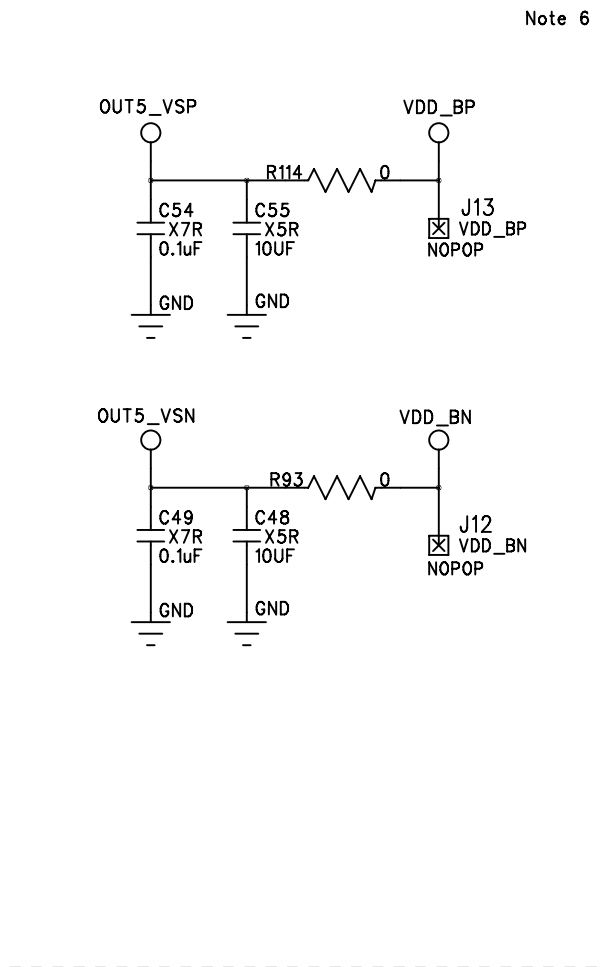
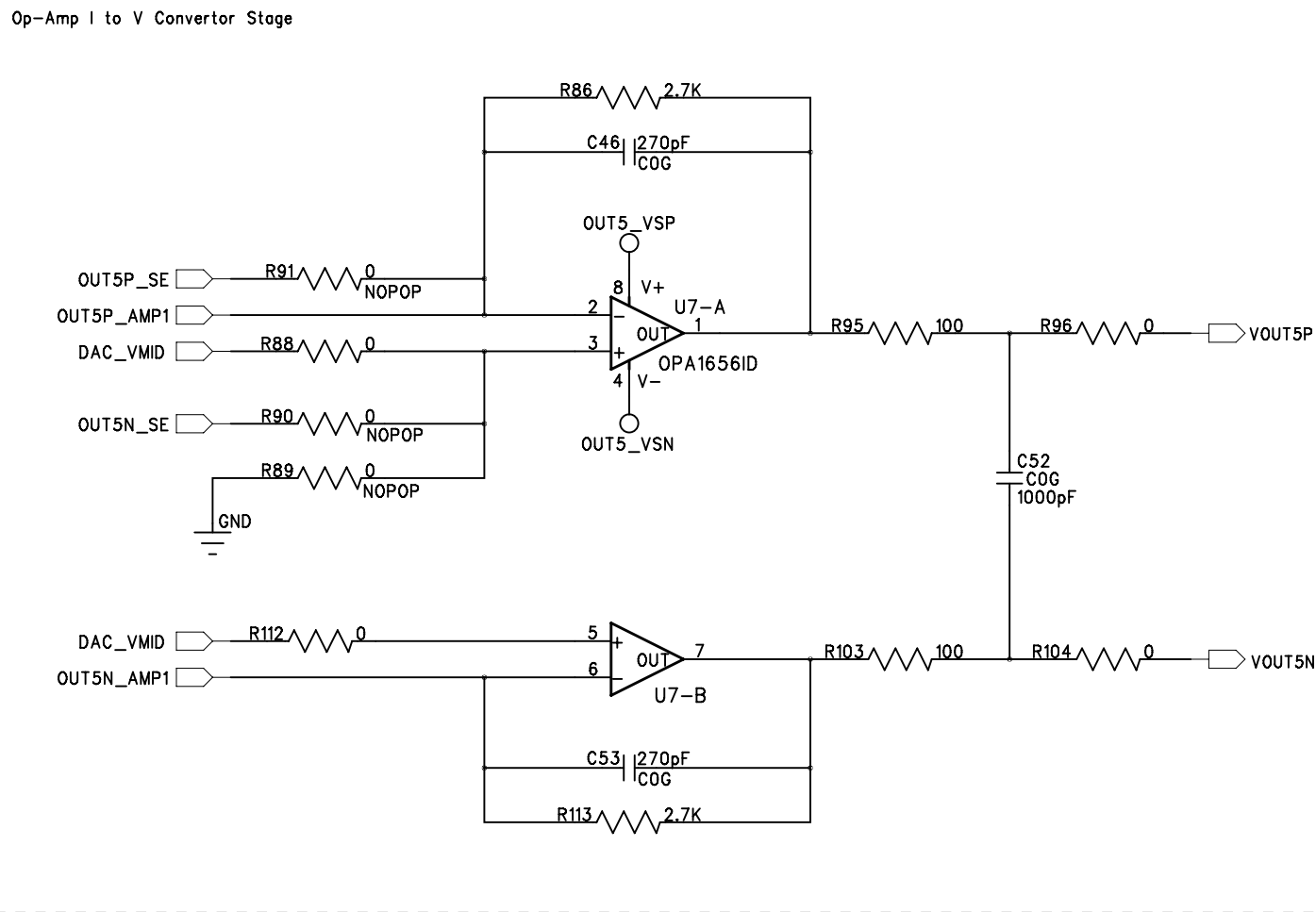
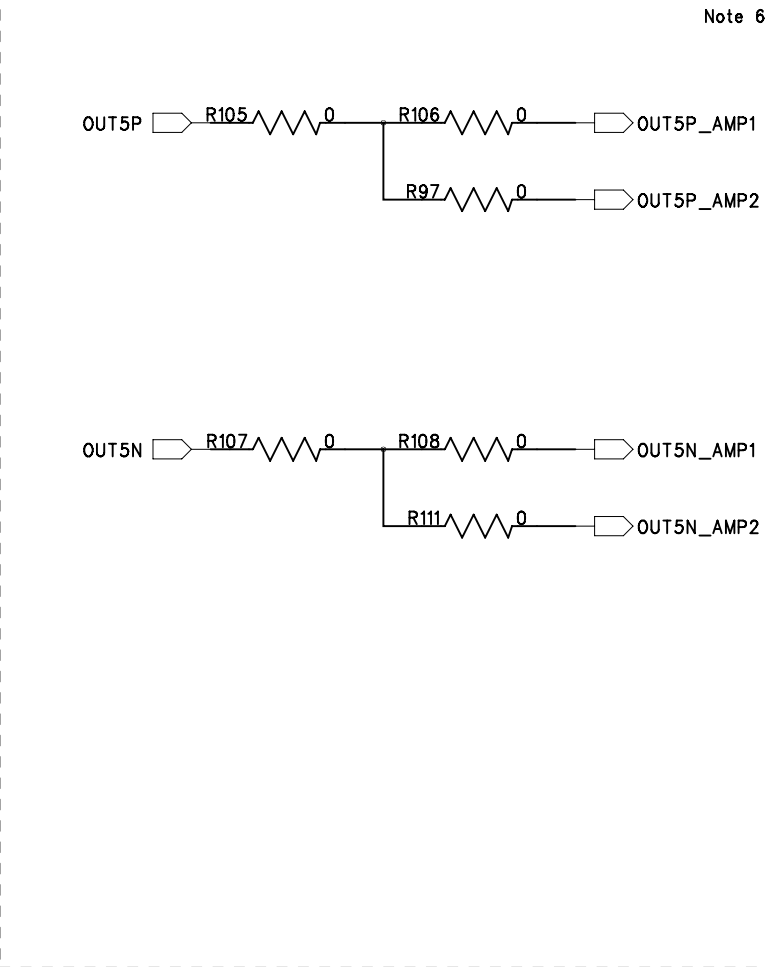
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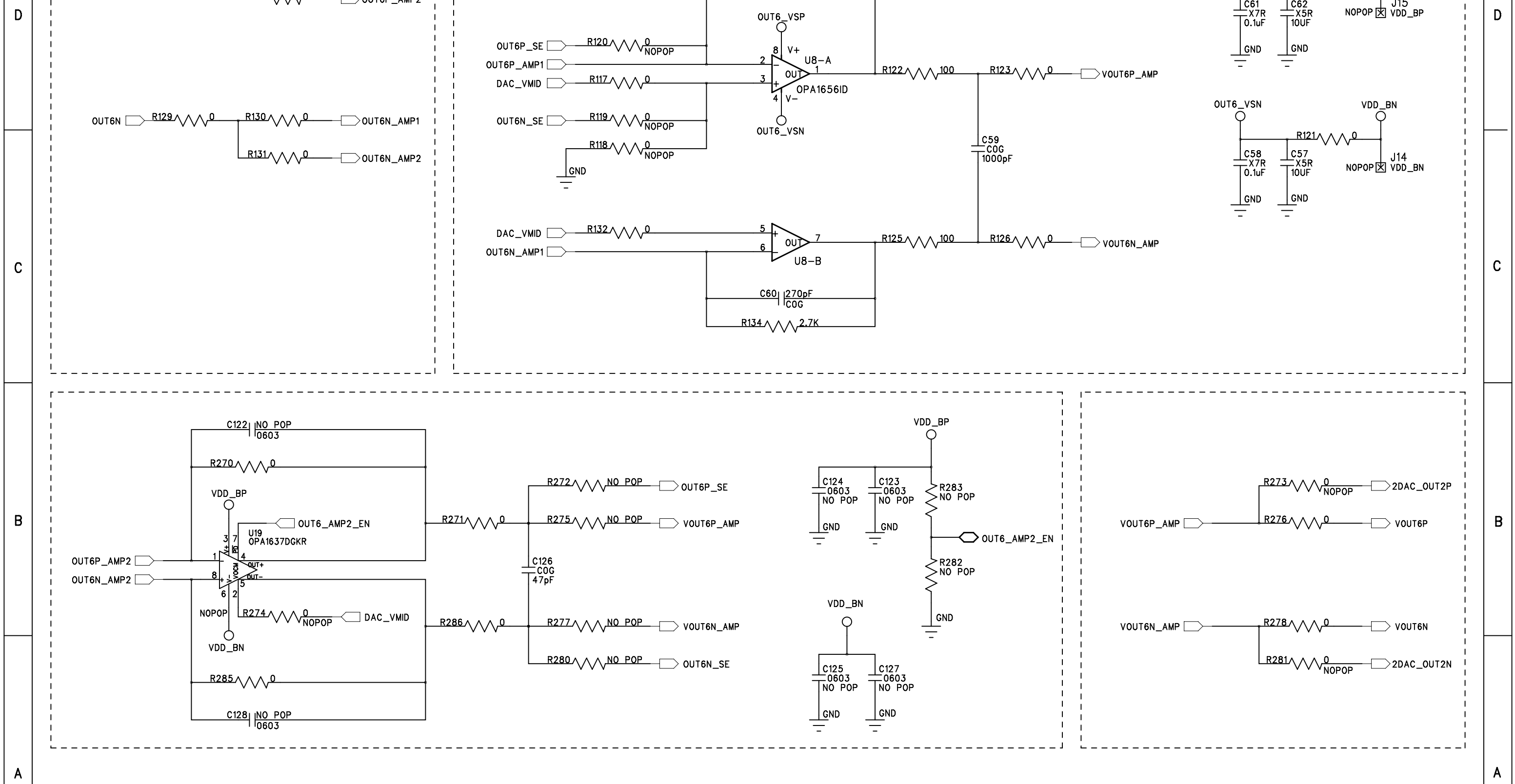
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CS430## Schematic Layout Guidelines



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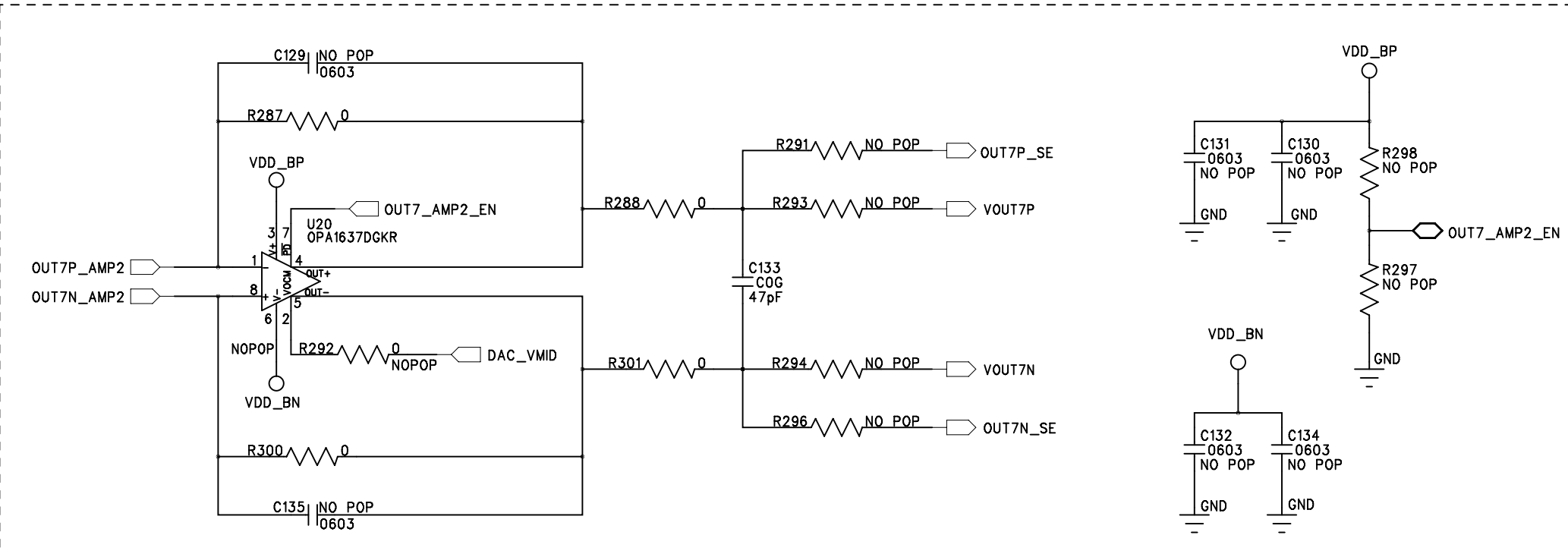
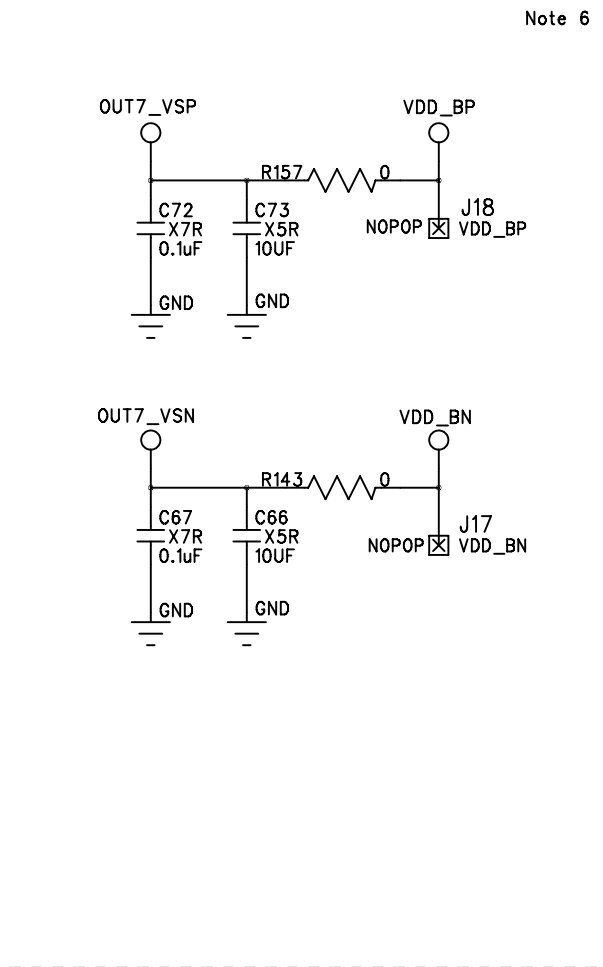
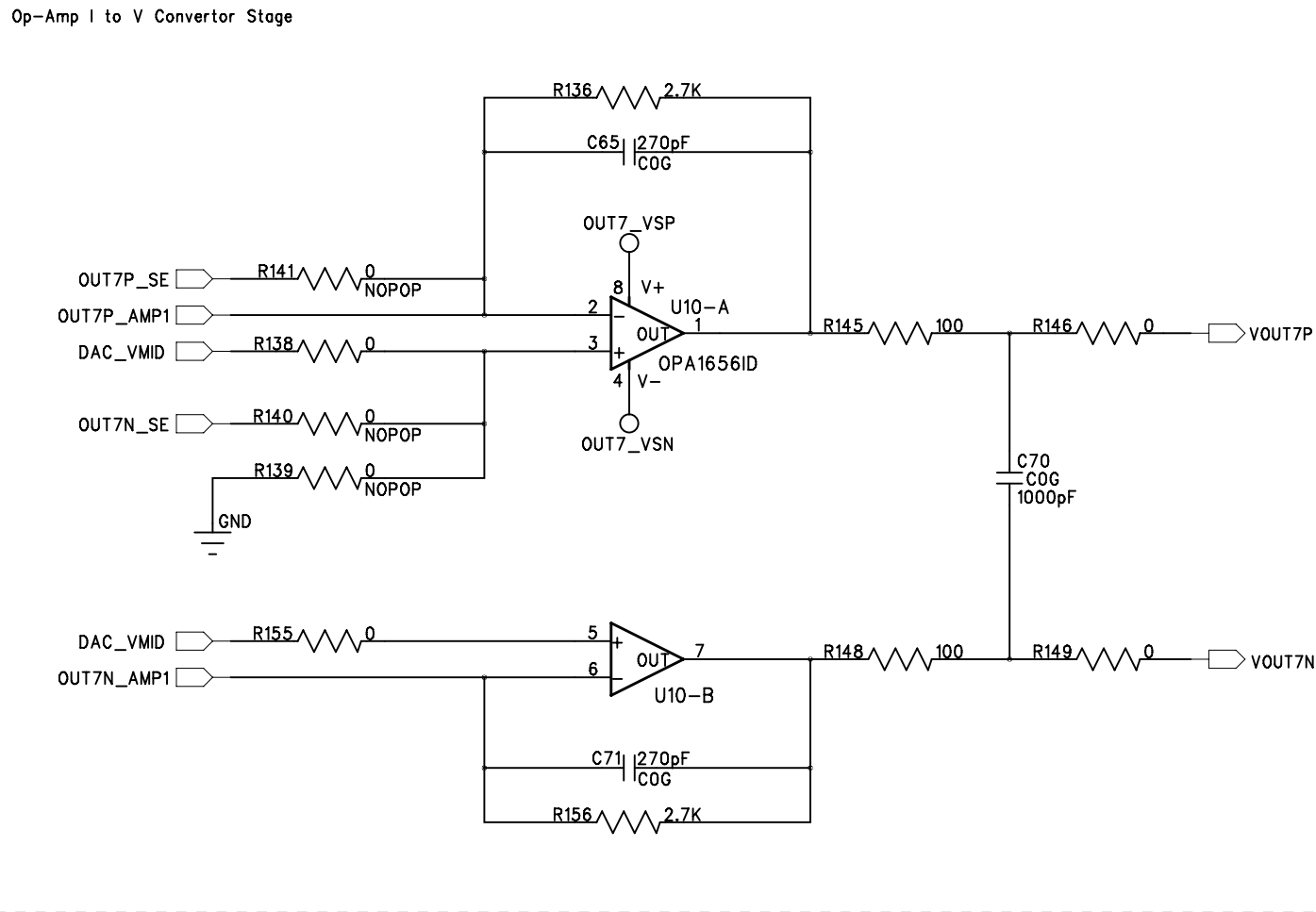
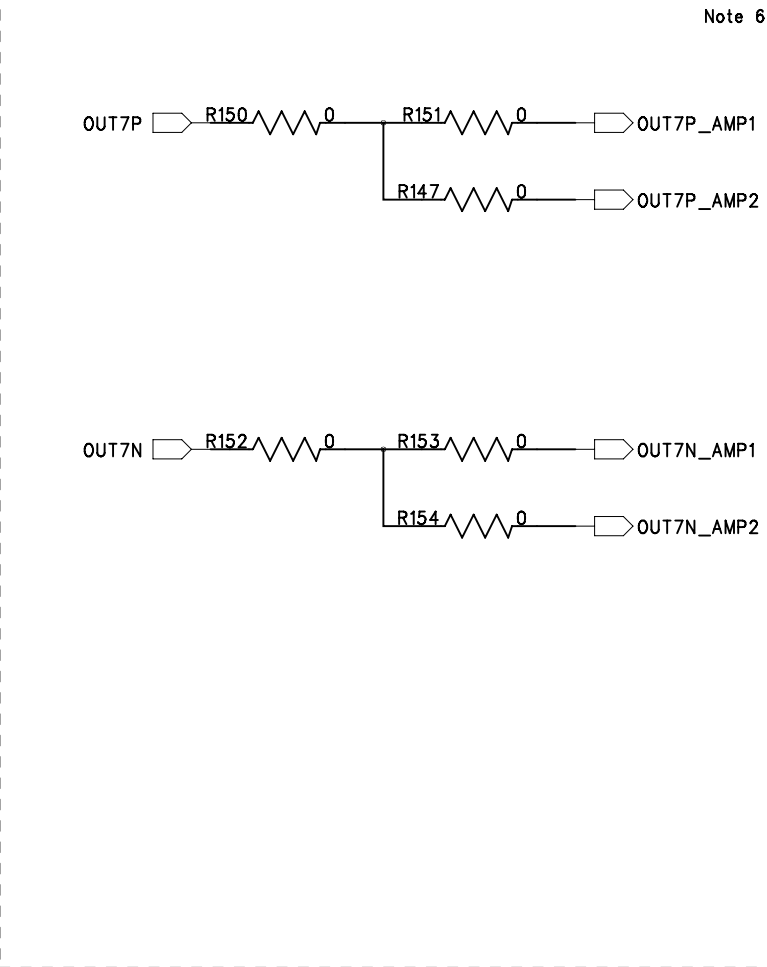
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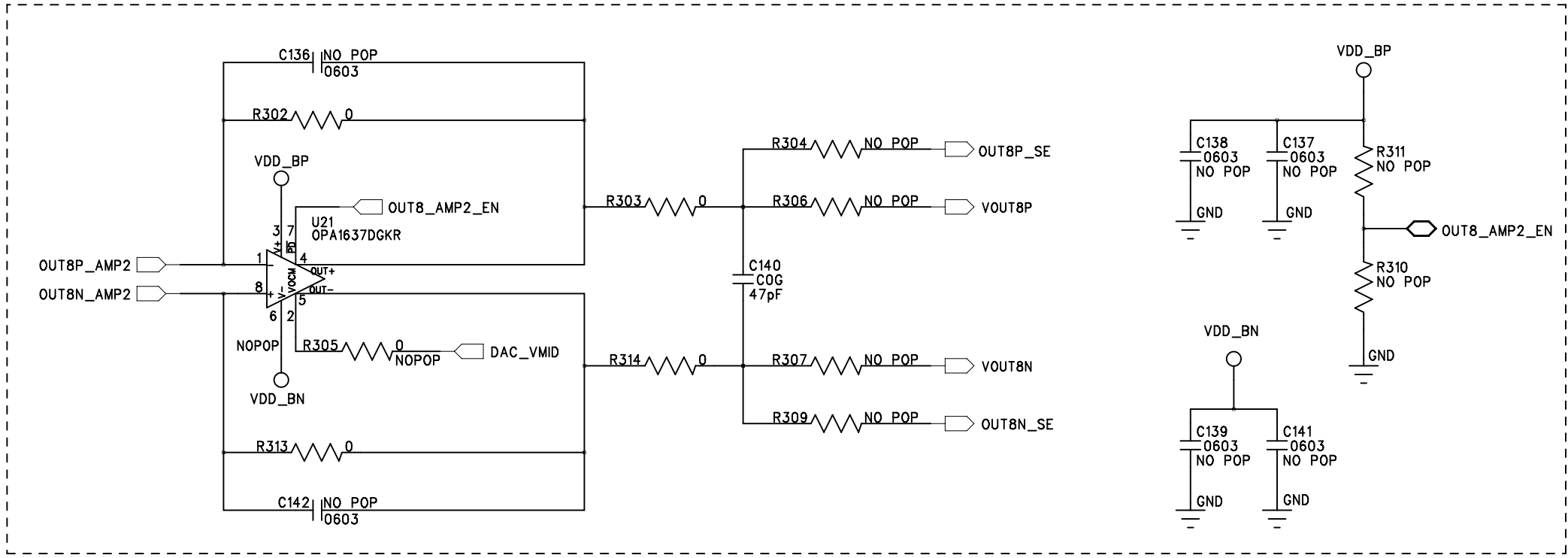
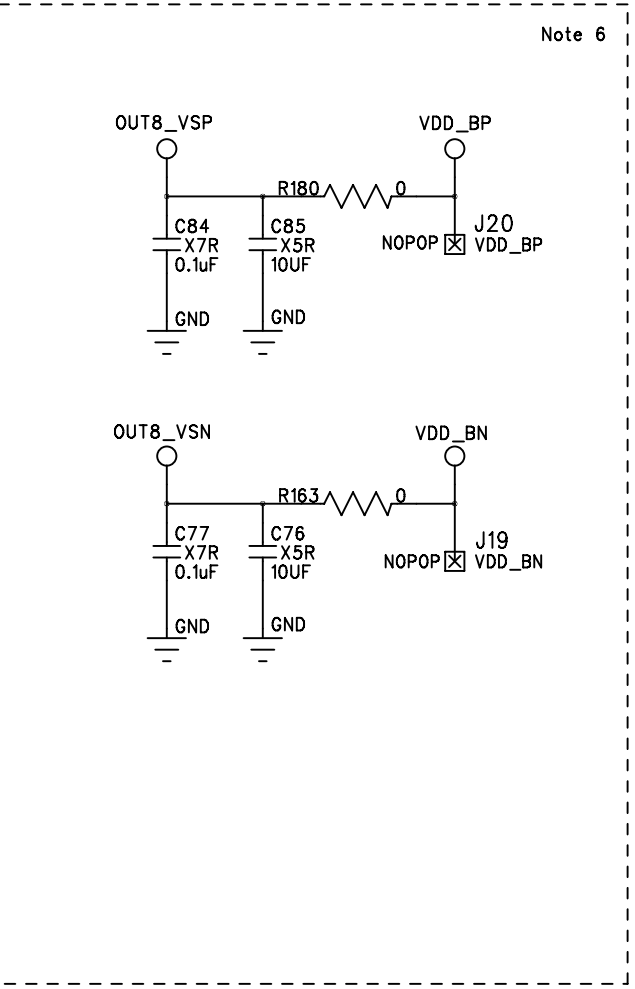
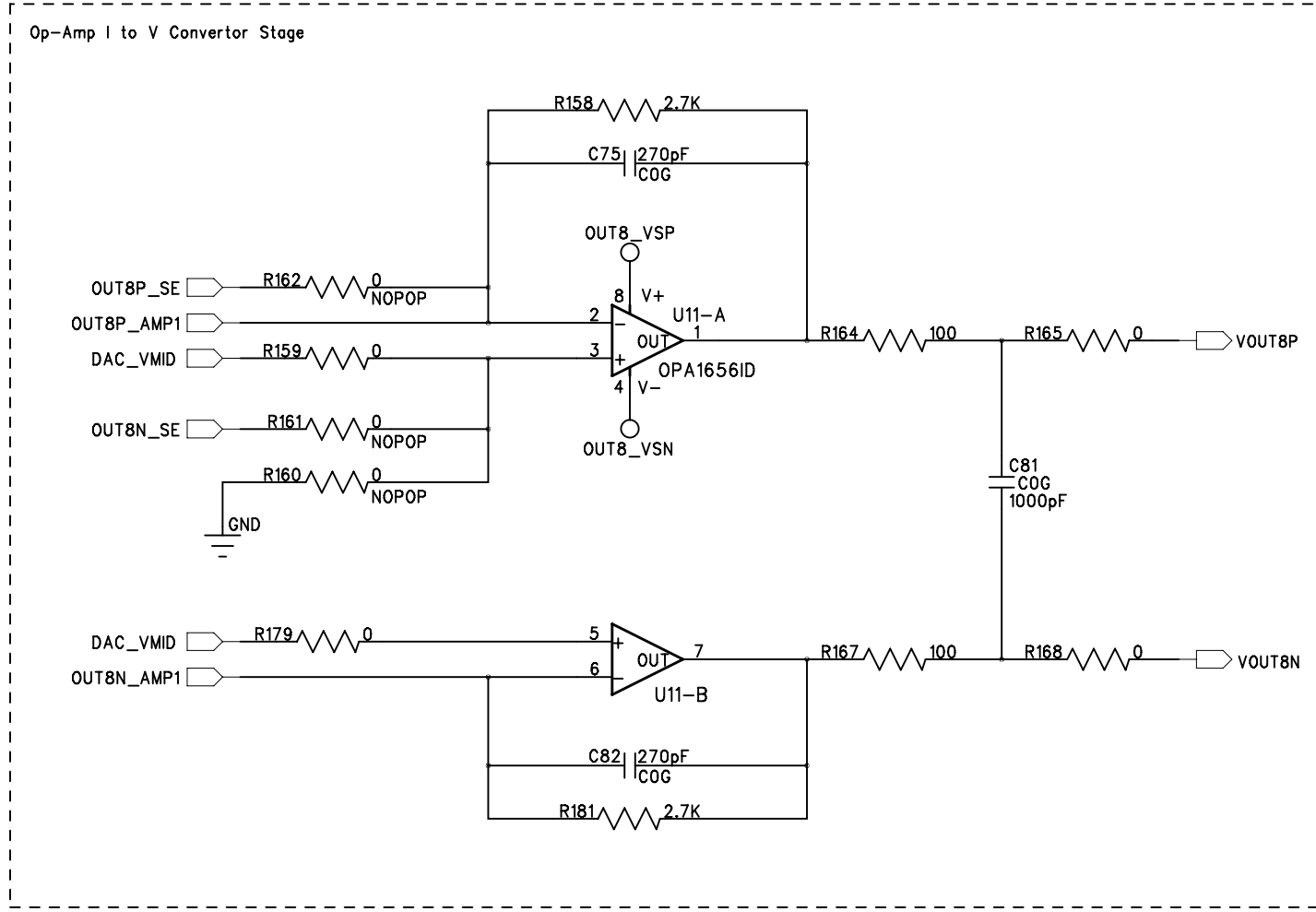
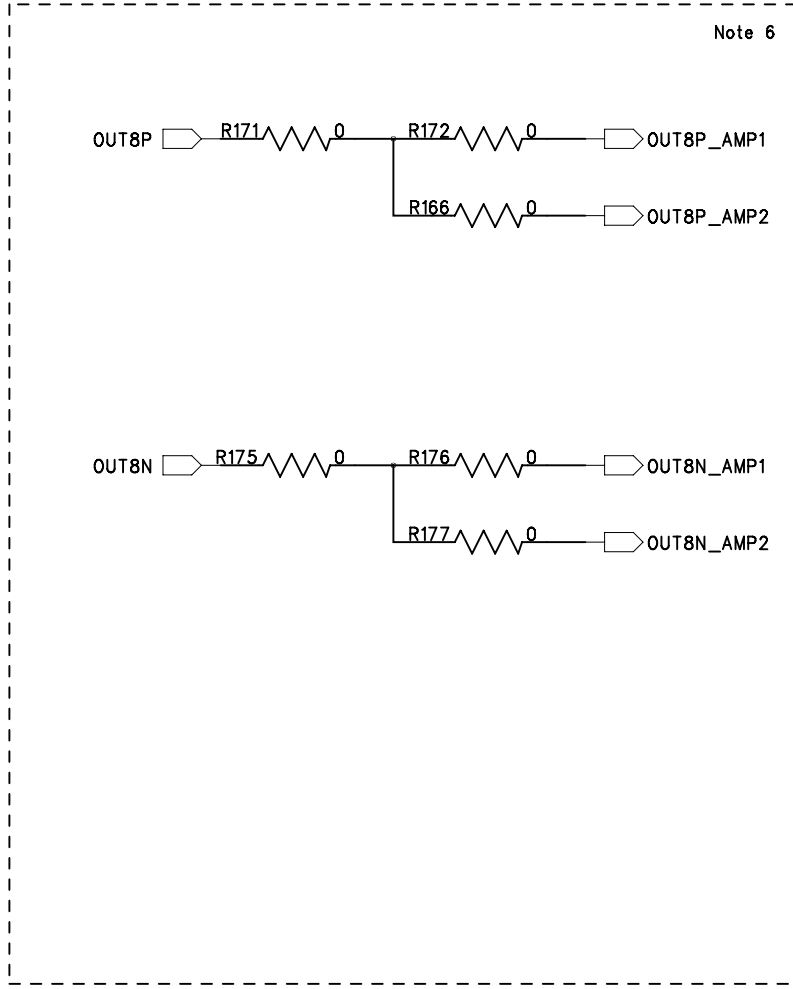
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